

made to measure

OPERATING INSTRUCTIONS AND SYSTEM DESCRIPTION OF THE

TURBO TEC-10CX TWO ELECTRODE CLAMP SYSTEMS

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0 SAFETY REGULATIONS

<u>VERY IMPORTANT</u>: Instruments and components supplied by npi electronic are NOT intended for clinical use or medical purposes (e.g. for diagnosis or treatment of humans), or for any other life-supporting syst em. npi electronic expressively disclaims any w arranties for such purpose. Equipmen t supplied by npi electronic shall be operated only by selected, trained and ade quately instructed personnel. For details please consult the GENERAL TERMS OF DELIVERY AND CONDITIONS OF BUSINESS of npi electronic, D-71732 Tamm, Germany.

<u>GENERAL</u>: This system is designed for use in scientific laboratories and should be operated by trained staff only. General sa fety regulations for operating electrical devices are to be considered.

<u>AC MAINS CONNECTION</u>: In working with instrum ents and components supplied by <u>npi</u> <u>electronic</u>, always adhere to the appropriate safety measures for handling electronic devices. Before using any device please read manuals and instructions carefully.

Always use a three-wire line cord and a mains power-plug with a protection contact connected to mains ground (protective earth).

Check for appropriate line voltage before connecting any system to mains.

Before opening the cabinet disconnect mains power-plug.

Disconnect mains power-plug when replacing the fuse or changing line voltage. Replace fuse only by appropriate specified type.

STATIC ELECTRICITY: Electronic equipment is sensitive to static discharges. Some input devices such as headstages are equipped with very sensitive FET am plifiers, which can be damaged by electrostatic charge and must therefore be handled with care. This can be avoided by touching a grounded m etal surface when cha nging or adjusting the electrodes. If a headstage is not used the input should always be connected to ground (by using an appropriate connector or with aluminum foil wrapped around the headstage).

VERY IMPORTANT: Always turn power off when connecting or disconnecting headstages or other components from the 19" cabinet.

<u>**CURRENT INJECTION HIGH VOLTAGE HEADSTAGE:**</u> The current injection headstage has a ± 45 V or ± 150 V output compliance. After turning on the instrument, it must be ensured that the interior contact and the shield of the electrode plug and of the cable which is connected to this plug cannot be touched.

VERY IMPORTANT: Always turn power off when changing or adjusting the electrodes.

TEMPERATURE DRIFT / WARM-UP TIME: All analog electronic systems are sensitive to temperature changes. Therefore all electronic c instruments containing analog circuits shall be used for recordings only in a warm ed-up condition (i.e. after internal tem perature has reached steady-state values). In most cases a warm-up period of 30 minutes is sufficient.

1 INTRODUCTION

About this Manual

This instructions manual describes the most important functions and operation possibilities of the TURBO TEC-05 and TURBO TEC-10 fam ily of Voltage/Current Clam p amplifiers. A short introduction to the theory and practice of the voltage clamp and current clamp technique is also included, as far as it is necessary for understanding the operation of this instrum ent. A broad selection of literature, of which we give a selection at the end of the m anual, is available on these techniques.

The manual is divided into 8 chapters (0,...,7) . Chapter 0 (Safety Regulations) gives som e hints for the safe operation of the instrum ent. Following this chapter (1 Introduction), in the chapter 2 (System Description), the functioning of the device is outlined, followed by chapter 3 (Controls and Connectors), which describe s the control switches and displays. The Installation chapter (4) prescribes the calibration and test procedures prior to and at the onset of an experim ent. The Control Theory Appendi x (chapter 5) describes som e theoretical aspects and provides hints for the tuning of the voltage clamp control unit, and in chapter 6 (References) a selection of literature is given. The last chapter 7 outlines the m ost important technical data of the TURBO TEC amplifiers series.

Important Literature

An excellent introduction to recording techniques, preparation of oocytes etc. can be found in "Methods in Enzymology, Vol. 207", (see ref. 21) and the chapter (2.19) by Stuehm er et. al. in ref. 10. The basics of m icroelectrode techniques and VC principles are described in a comprehensive m anor in the "Plymouth W orkshop Handbook (Ogden, 1996, see ref. 11). Please refer to chapter 6 (REFERENCES) f or a more detailed list of literature or please contact npi electronic.

Software

The normal experimental situation is the use of a com puter-based data acquisition system for controlling the experiment. Nevertheless all TEC systems are designed in a way that they can be used without software. A stimulus generator, digital oscilloscope and chart recorder would be sufficient for working with these amplifiers (see Fig. 4).

All TEC system s can be used in conjuncti on with the various software packages commercially available. Input and output signals have calibrations that are suitable for m ost data acquisition packages. They also provide special features such as electronic (rem ote) selection of m odes of operation and m onitor (t elegraph) signals for the position of current gain and filter switches. Their input and output features as well as the m onitor (t elegraph) signals provide allow very com fortable interaction between the clam p instrum ent and the data acquisition package.

If the software CellWorks (available from npi electronic) is used, some of the functions of the TEC systems can be controlled directly from the computer.

2 SYSTEM DESCRIPTION

2.0 GENERAL DESCRIPTION

The TURBO TEC instruments are voltage/current clamp systems which function according to the classic dual-m icroelectrode m ethod. This m ethod uses one m icroelectrode for the registration of m embrane potential and one for cu rrent injection. The equivalent circuit of a TEC system and the associated block diagram in VC mode are shown in Fig. 1 and Fig. 2. A view of the TEC front panel is given in Fig 3.

Each amplifier of the Turbo TEC series is m ade up of a 19" basic system with a built-in power supply and two m easuring headstages: A smaller one for potential recording and one for current injection and recording.

All TEC system s are based on m odern state-of-t he-art electronic circuits. Their advanced design makes them superior to other amplifiers. Some of the special features of TEC systems are: differential potential registration, high-voltage current source output, both to elim inate artifacts induced by the use of m icroelectrodes. TEC system s have autom ated electrode resistance test modes which can be used even with the electrodes impaled in a cell (see article by Stühmer et. al, Methods in Enzymology, Vol. 207). In addition a unique oscillation shutoff circuit prevents the cell from damage if oscillations occur.

Since the voltage and current clam p techniques are standard techniques of electrophysiology (for a review see Methods in Enzym ology, Vol. 207, Sm ith et al., 1985, or Standen et al., 1987, Kettenm ann & Grantyn, 1992, Ogden 1994), only a short procedural description follows based on the diagrams of Fig. 1 and Fig. 2. Terms and abbreviations in capital letters in the text correspond with the labels on the front panel.

Selection and Options

The following versions are available: TEC 03X: standard oocyte amplifier (separate manual available) TEC 03X-CW: special version for CellWorks software TEC 05X: large cells, muscle, invertebrate cells with fine tipped electrodes, please contact npi electronic for details) TEC 05CX-CW: special version for CellWorks software TEC 10X: TEC 10 with digital control of cu rrent filter and gain, without transient compensation unit TEC 10X-CW: special version for CellWorks software TEC 10XX-CW: special version for CellWorks software TEC 10CX: TEC 10 with digital control of current filter and gain TEC 10CX: Special version for CellWorks software

Accessories

TEC systems are delivered with two headstag e, power chord, m anual and a set of cables / connectors for the reference, ground and the curre nt electrode connector. Special headstages, microelectrode holders and cell m odels ("TEC Cell Model") are available on request. Please refer to chapter 7 for details or contact npi electronic for details.

2.1 POTENTIAL REGISTRATION

Arrangement of the Recording Electrodes

For m embrane potential registration all TEC amplifiers use a differential electrode arrangement to record the membrane potential as accurately as possible (Fig. 1 and Fig. 5). A description of the potential headstage can be found in chapter 3 (3.1).

Two electrodes, an intracellular m icroelectrode (P $_{EL}$ = potential electrode) and an extracellular electrode (REF= reference electr ode), which are connected to high im pedance buffers (input resistance better than 10 13 Ω) in the potential headstage are required for potential measurement. In addition the bath surrounding the cell m ust have a severe ground connection (Ag/AgCl pellet or Agar bridge, s ee Fig. 5) which can carry the large m embrane currents flowing during voltage clam p experiments. This arrangem ent ensures the m ost accurate m easurement of the transm embrane potential: the reference electrode (REF) measures the bath potential (extracellular potential) which is subtracted from the intracellular potential recorded by the intracellular electrode (P $_{EL}$). Intracellular m icroelectrodes used for oocytes have resistances of 300 k Ω up to 1-2 M Ω , the resistance of the REF electrode is usually much smaller (a few ten k Ω maximum).

Capacity Compensation

The frequency response of the potential electrode (low-pass characteristic) is compensated for by a feedback circuit ("negative capacity " compensation, CAPACITY COMPENSATION , 10-turn potentiometer) and a "driven-shield" a rrangement (for an overview see Ogden 1994). The dial is not calibrated and has its zero position on 000. Since in oocyte experiments microelectrodes are usually in the one M Ω range or below for most experiments it is not required to use capacity compensation.

WARNING: Capacity com pensation is based on positive f eedback. Theref ore overcompensation causes oscillations (ringing) wh ich can deteriorate the preparation or the recording electrodes. Therefore, the control m ust be handled with care and before im paling a new cell must be set to 000.

Offset Compensation

All microelectrodes produce a potential by them selves, the tip potential. This nonlinear and must be compensated electronically. The tip potential of the potential registration microelectrode (P_{EL}) is equalized for the most part autom atically through the differential potential registration.

The remaining offset is compensated manually by adjusting the offset compensation controls which is available for each electrode (10-turn potentiom eters, POTENTIAL OFFSET, approximately \pm 200 mV and CURRENT ELECTRODE OFFSET, approximately \pm 500 mV).

WARNING: Of fset controls are symmetrical (operating both in positive and negative direction), therefore setting to zero occurs on position 5.00 on the dial. 10 is maximum in positive direction 0 is maximum value on negative direction.

Current Electrode Potential Recording

In order to determ ine whether both electrodes are inserted into the sam e cell, the potential of the current injecting m icroelectrode (C_{EL}, see 2.2) is recorded by a buffer am plifier in the current headstage with a x10 m V scaling (C el. POTENTIAL x10 m V BNC). This unit is equipped with an offset compensation (C_{EL}. OFFSET, ten-turn potentiometer,. +/- 500 mV).

<u>WARNING</u>: Due to the lim ited operation range of the output am plifier (± 12 V m aximum) the high voltage signals occurring during curre nt injection will drive this output into saturation (clipping). Therefore during electrode positioning no current flow should occur through the current electrode (C_{EL}). During voltage clamp mode the signals at this output may become very noisy.

WARNING: The C_{EL} Offset control is symmetrical (operating both in positive and negative direction), therefore setting to zero occurs on position 5.00 on the dial. 10 is m aximum in positive direction 0 is maximum value on negative direction

Potential Monitor and Audio Monitor

The measured membrane potential is amplified, P_{EL} by a factor of 10 or 40, C_{EL} by a factor of ten. The recorded potentials from both microelectrodes (P_{EL} and C_{EL}) can be read out from the respective BNC-sockets (POTENTIAL OUTPUT P _{EL} x10 or P _{EL} x40, and C _{EL} x10), and can also be directly (not am plified) displayed in mV on a digital display. The electrode which is monitored at the display is selected by a toggle switch located at the left side of the display (POTENTIAL ELECTRODE or CURRENT ELECTRODE).

In addition the recorded potentials are converted to a sound with a potential dependent pitch with the AUDIO MONITOR. The electrode which is monitored by the AUDIO MONITOR is also selected by toggle switch POTENTIAL ELECTRODE or CURRENT ELECTRODE.

2.2 CURRENT INJECTION AND CURRENT MEASUREMENT

Current Injection

The current injection is perform ed by means of a glass microelectrode which is connected to the current headstage (C_{EL}). A description of the current headstage is given in chapter 3.2.

The unique advantage of the instrum ents in the Turbo TEC series is the voltage-controlled current source output (V/C or V/I converter), for electrical compensation of the disturbances from the m icroelectrode during current injection (i.e. high resistance and stray capacity, see Polder, 1984, Polder & Swandulla, 1990). This current source is built into the current headstage. Due to this current source output, current injection becom es independent from the resistance of the m icroelectrode which is usually strongly nonlinear. In addition, this circuit allows direct measurement of the current injected in the cell without the necessity of a virtual ground circuit for the bath.

Output Current Zero (C. HEADSTAGE BIAS CURRENT)

In order to adjust the zero-current of the out put current source, each instrum ent provides a bias current adjustment with a 10-turn potentio meter (ca. $\pm - 0.5\%$ of the current range). The tuning procedure is described in the INSTALLATION chapter.

Current Injection Bandwidth

On all TEC systems for oocyte recordings, the ba ndwidth of the current injection electronics can be lim ited to approxim ately 10 Hz by m eans of a switch (BANDW .) on the current headstage (see Fig. 5). This allows the use of a patch clam p amplifier for the recording of channel currents sim ultaneously to m acro-currents recorded with the TEC system without excessive noise from the two electrode clamp loop (see 2.5, Low Noise Mode).

WARNING: If the bandwidth of the current headstage is set to 10 Hz, som e functions such as R_{Cel} (current electrode resistance test) do not work properly.

Capacity Compensation (current electrode, optional)

The TEC-05X amplifier are equipped with a cap acity compensation for the current electrode (CUR. EL. C-COMP., 10-turn potentiom eter). This increases the speed of the voltage clam p control circuit when using high-resistance (>1 M Ω) microelectrodes.

WARNING: Capacity com pensation is based on positive f eedback. Theref ore overcompensation causes oscillations (ringing) wh ich can deteriorate the preparation or the recording electrodes. Therefore the control m ust be handled with care and before im paling a new cell must be set to 0.

Current Range

Since the resistances of the microelectrodes are in the M Ω range, it is necessary to use a high voltage current source (+/- 150) for current in jection. The TEC standard version has an output compliance of ±150 V, i.e. the maximum current is 150 μ A / 1 M Ω . The current range of the various TEC versions is determined as follows:

<u>TEC 05X</u> 150 nA /100 M Ω or 1.5 μ A / 10 M Ω (for large cells, e.g. invertebrate neurons)

TEC <u>10CX</u> 150 μ A /1 M Ω (for very large cells, e.g. oocytes)

Some current headstages are equipped with a switch for the selection of different current ranges (see options, below). For the standard 150 V headstage the ranges are:

Option 1 (x0.	1, x1, x2, x5):	Option 2 (x0.	1, x0.2, x0.5, x1)
x0.1 range:	15 μΑ / 10 ΜΩ	x0.1 range:	$15~\mu A$ / $10~M\Omega$
x1 range:	150μΑ /1 ΜΩ	x0.2 range:	$30~\mu A$ / $5~M\Omega$
x2 range:	300 μA/500 kΩ	x0.5 range:	$75~\mu A$ / $2~M\Omega$
x5 range:	500 μA/200 kΩ	x1 range:	$150 \mu A / 1 \; M \Omega$

<u>WARNING!</u> Always adhere to the appropriate sa fety m easures (see Safety Regulations, Introduction and Installation chapters) when usi ng these instrum ents. In particular, always shut power off when changi ng or adjusting electrodes! <u>Always turn pow er off w hen</u> <u>connecting or disconnecting headstages or other components from the 19" cabinet</u>.

Current Measurement

The use of the current source output allows that the current is measured en route to the electrode, an improvement in accuracy on the "virtual ground" method, which requires an additional headstage. The current source method also provides an improved frequency response of the voltage clamp control circuit.

Current Monitor and Current Output Sensitivity

The TEC systems have two current output s: CURRENT OUTPUT UNCOMPENSATED, the current signal directly obtained from the h eadstage, $(0.1 \text{ V}/\mu\text{A}, \text{ i.e. } 1 \text{ V} \text{ at this BNC}$ corresponds to a current of 10 μA injected into the cell, standard version). The current from the headstage is also displayed on the digital di splay (lower display). This signal is am plified and filtered for better presentation giving the CURRENT OUTPUT signal.

The am plification is perform ed by an am plifier with seven gain f actors (f rom $0.1V/\mu$ A.... $10V/\mu$ A which corresponds to an amplification of x1/ x2/ x5/ x10/ x20 /x50 x100). The selection can be set by a rotary sw itch (CURRENT OUTPUT SENSITIVITY). The following calibrations for the signal at the CURRENT OUTPUT BNC result: TEC 05X: 0.1, 0.2, 0.5, 1, 2, 5 and 10 V/nA; display XX.XX nA 10CX: 0.1, 0.2, 0.5, 1, 2, 5, and 10 V/µA;

The position of the CURRENT OUTPUT SENS ITIVITY switch is m onitored by a DC voltage (1 V / switch position) 1V...7V av ailable at the rear panel (MONITORING OUTPUTS, CURRENT OUTPUT SENSITIVITY BNC connector). Example: A current sensitivity of 1 V/ μ A at CURRENT OUTPUT BNC corresponds to a voltage of 4 V at the CURRENT OUTPUT SENSITIVITY BNC connector.

Transient Compensation (TEC 10CX)

The <u>TURBO TEC 1</u> θCX series provides a compensation unit for the suppression of the capacitive currents in the voltage clam p m ode (CURRENT TRANSIENT COMPEN-SATION). For the T<u>URBO TEC-05X</u> the compensation unit is available as a separate instrument for the modular EPMS-07 system (TRC-01M).

Through this compensation unit the voltage clamp pulse is differentiated with three adjustable time constants and added to a linear component to compensate for the leakage current. This signal is subtracted from the measured current signal. The adjustment is made through 4 potentiometers for the amplitudes (A1, A2, A3 and linear) as well as 3 10-turn potentiom eters (T1-T3) for the time constants of the differentiators. A BYPASS switch allows to quickly switch off the compensation except the linear component.

Current Filter

A tunable low-pass (CURR. FILTER) is attached to this amplifier.

The TEC 10CX has a four-pole low-pass Bessel f ilter with 12 (or 16) corner frequencies: 20, 50, 100, 200, 300 500, 700, 1k, 1.3k, 2k, 3k, 5k, (8k, 10k, 13k, 20k) Hz. The position of the switch is monitored by the FREQUENCY M ONITOR signal (-5...+6V, 1V/step, 12 position filter and -8...+7V, 1 V/step, 16 position filter).

In the TEC 05 it m ay be a single-pole or a f our-pole Bessel f ilter with 12 or 16 corner frequencies (see TEC 10CX).

Options

Some of the Turbo TEC instrum ents have current headstages with four ranges (see also page 10). The TEC-10 m odel can also be purch ased without transient compensation. Please ask npi for details.

2.3 ELECTRODE RESISTANCE MEASUREMENT

Resistance measurement modes for both m icroelectrodes (R_{Pel} and R_{Cel}) are included in this device in order to test the function of the electrodes. These test units operate independently of any other adjustm ents. This is possible under the condition that all m icroelectrodes are in contact with a grounded bath (zero potential). The m easured resistance is independent of tip potentials and is automatically displayed on the digital display in M Ω .

The m easurement is perform ed by applying square current pulses of a few nA to the respective m icroelectrode. The voltage deflecti on caused by this injection is recorded and processed to give a direct reading in M Ω on the digital display. *The electrode resistance test is also a test of the correct function of the respective headstage.*

<u>**WARNING**</u>: RESISTANCE TEST m ode gives only a correct value with high resistance electrodes if the capacity is compensated correctly. Furthermore, with a headstage with four ranges (see above) the RESISTANCE TEST has to be carried out in x1 position. Only in x1 position the value is displayed correctly.

2.4 CURRENT CLAMP MODE (CC)

In the current clam p m ode, the cell' s reaction to current injections is m easured. Current injection is perform ed by m eans of a current source connected to the current injecting microelectrode, regardless of the electrode re sistance (see Fig. 1). Therefore only a current input conditioning unit is necessary for the ad equate shaping of the current input signal (COMMAND INPUT). All instrum ents are equipped for the injection of a constant current (HOLD control, X.XX μ A, adjustable through a 10- turn potentiometer with a digital display), and with an analogue input. The polarity is controlled by a switch, with which the HOLD current signals can also be turned off.

Current Clamp Inputs

The inputs are analogous to those of the voltage clamp mode: A constant holding current is set on the 10-turn HOLDING potentiom eter with a μ A display (X.XX μ A, i.e. m ax. range is 9.99 μ A). The polarity of the HOLDING control is controlled with the -/0/+ switch. In the 0 position the HOLDING control is turned off. The analogue current input is calibrated with 1 μ A/V, i.e. 1 V generates a current of 1 μ A. This input is controlled by an ON/OFF switch.

2.5 VOLTAGE CLAMP MODE (VC)

In the voltage clam p mode, the m embrane potential is f orced by a controller to m aintain a certain value or to follow an external co mmand, which allows m easurement of ion fluxes across the cell m embrane independent of potential changes and separate f rom capacitive current flows. This is the m ost complex mode of operation with these instrum ents. Special precautions must be taken while tuning the cont rol circuit in order avoid stability problem s. **IMPORTANT:** Although in VC m ode one is prim arily interested to record the current flowing across the m embrane the clam p circuit controls primarily membrane potential. The better the potential is controlled i.e. the sm aller the VC error signal (com mand signal m inus recorded signal) can be m ade, the m ore accurate on can record m embrane currents. Som e theoretical aspects are presented in the **Control Theory Appendix** (see also references).

Voltage Clamp Inputs

The inputs are analogous to those of the current clamp mode: A constant holding potential is set on the 10-turn HOLDING potentiom HOLDING control is controlled with the control is turned off. There are two anal ogue inputs: one is calibrated with 10 mV/mV analogous to the x10 mV potential output a nd the second is calibrated /40 (x0.025) corresponding to the x40 output. The inputs ar e controlled with the respective ON/OFF switches.

Rise Time Control

With application of a pulse, the m aximum ri se time can be limited with a control. The calibration corresponds to a pulse of 100 mV (1 V on :10 mV command input). The rise time limit is necessary to dam pen the overshoots while calibrating the control circuit according to the "symmetrical optimum" (described in the *Control Theory Appendix*).

Control Circuit (P-I controller)

The <u>TURBO TEC</u> systems are equipped with Proportional-Integral (PI) control loops. These compare the measured membrane potential with the set command potential. The difference is then amplified and integrated by means of an integrator (for details see refs. 5, 12, and 13).

Consequently, amplification of frequencies with a value less than the cut off frequency of the integrator (reciprocal to the time e constant) becomes very large, which then positively influences the control process. The resulting signal is used as the command for the current source (see Figs.1 and 2) and determent ines the current injection, which approaches the set command membrane potential because of the polarity change in the control circuit.

More details about the functioning of the PI controller are given in the Control Theory Appendix 5.1. The proportional gain is set with the potentiom eter "GAIN"; the time constant of the integrator is set with the potentiom tiometer INTERGRATOR TIME CONSTANT. The integrator can be shut off with a toggle-switch. In the Control Theory Appendix 5.2, the adjustment of the PI control loop is elaborate d. Some considerations concerning the speed of response and linearity are given in Appendix 5.3.

VC Error Display

The recorded m embrane potential is com pared with the com mand input signal with a differential amplifier giving the VC error signal. This signal is applied to the controller where it is am plified and f ed back into the current source feedback signal). The feedback signal is converted into a current injected through the curre nt electrode into the cell to com pensate the ionic fluxes across the cell m embrane (sym bolically called "active" in Fig. 1 and "Cell activity" in Fig. 2). under stable conditions the injected current corresponds directly to the ionic m ovements across the cell m embrane. The VC ERROR display shows directly the deviation of the recorded potential from the command signal. For an accurate clam p it must be between ± 0.5 (max. ± 1).

Current Limit (VC OUTPUT LIMIT)

Under certain experimental conditions, it is necessary to limit the current in the voltage clamp mode (e.g. in order to prevent the blocking of the electrode or to protect the preparation). This is possible with an electronic limiter, which sets the current range between 0-100%.

Series Resistance Compensation

With som e preparations, it is not always possible to exclude series resistances, despite differential potential recording (see Fig. 1). These series resistances could cause a current-proportional potential error in the voltage clam p m ode, i.e. an unwanted change in the membrane potential during a current flow. This change can be partially com pensated by current-proportional amplification in the control circuit. This is done by positive feedback in the control circuit, which can very quickly lead to stability problem s. Re-positioning the electrodes is recommendable whenever possible; use this compensation procedure only as a last resort.

Low Noise Mode

TEC instruments can be used in a low noise m ode for simultaneous recordings with a patch clamp amplifier. For this purpose the bandwidth of the current source which perform s the charge injection into the cell can be lim ited to 10 Hz by use of the BANDW. Switch located on the current headstage. In this m ode the clamp circuit is capable of following only slow changes, i.e. to keep the steady-state.

IMPORTANT: The controller m ust be used in P-m ode (INTEGRATOR = OFF) since parasitic oscillations m ay occur due to the limited bandwidth of the current source (two integral components in a closed loop form an oscillator, see ref. 5 for details).

WARNING: If the bandwidth of the current headstage is set to 10 Hz, som e functions such as RCel (current electrode resistance test) do not work properly.

Improvement of the Control Properties

Control circuits with negative f eedback tend to be instable as a result of delays immanent in the system (e.g. low-pass characteristics of the microelectrodes) or positive feedback caused by capacitive couplings between the electrodes. With voltage clam p systems, the control properties can be substantially im proved by shie lding the electrodes from each other. Often the shielding of the potential electrode suffices to reduce the coupling capacity between the electrodes. This shield can be connected to th e output of the buffer am plifier ("driven shield" arrangement, see Fig. 1). The correct setting of the C-com pensation increases the speed of response of the control loop, but also increases the noise. The correct setting of the different parameters results in a com promise between the stability, accuracy, noise, and control speed. Adjustment criteria, speed of response and lin earity are discussed in the Control Theory Appendix (5).

2.6. ADDITIONAL SYSTEMS

Audio Monitor

The membrane potential or the potential of the cu rrent electrode can be translated into an acoustic signal (voltage to frequency conversion). Not all instrum ents are equipped with this device. The signal from both electrodes can be connected to the AUDIO MONITOR, the selection is performed by a toggle switch. The volume can be set by a potentiom eter located on the left side of the front panel.

Oscillation Shut-Off

This system shuts of f the current injection and the C-compensation if oscillations that m ight damage the preparation appear. This m ay happen if the capacity compensation is set at too high levels or if the voltage clamp gain is too high.

The threshold at which the shut-off system is activated can be set in advance with a potentiometer (THRESHOLD). The correct setting must be found by trial and error.

A green light shows the correct function of the amplifier (i.e. normal operation is possible); a red one shows when it has shut down.

If the red light is on the system must be reset with the DISAB LE/RESET switch. In the DISABLED position the shut-off function is turned off (green LED on).

WARNING: If the red light is on, only the electrode resistance test and the potential registration and display unit work. All other functions of the am plifier are shut of f, i.e. it cannot be used any more for VC or CC recordings. In the DISABLED position the green light is always on (i.e. all amplifier function are activated, if oscillations occur, the preparation may be damaged).

2.7 DIGITAL CONTROL UNIT

All signal in the TEC instrum ents are select ed and com muted by electronic devices (analog switches and multiplexers) which are controlled with digital signals. This allows synchronous switching procedures that avoid switching artifacts and facilitates the use of the instrument. Many functions can also be controlled by com puter signals. A digital control interface is available. Please contact npi electronic for details.

Mode of operation selection

TEC-10CX systems: The selection can be made manually or remotely using TTL signals. The selected function is displayed by LED's.

On TEC-10CX systems, the selection of the four operation modes R_{Cel} , CC, VC, R_{Pel}) can be made with push-buttons, with LED display.

Remote selection can be made with TTL trigger signals applied to the respective inputs (rear panel).

Software selection (optional TEC-10CX-CW, from the CellW orks software package, see Chapter 1) W ith the ON/EXTERN switch located below the current f ilter the selection between front panel control (ON) and software control (EXTERN) can be selected.

On TEC-05X systems a 6-position switch is used for the selection of the m ode of operation. The active mode is indicated by LED's.

The switch labeled MODE OF OPERATION (located below the displays) is used to select DHC /VC /CC /OFF /BRIDGE or EXTERN mode. DHC and BRIDGE mode are optional. In the EXTERN mode the mode of operation can be determined by a TTL pulse applied at the MODE SELECT INPUT BNC connector (LO=CC, HI=VC).

If connected to CellWorks the system can be controlled from software.

3 CONTROLS and CONNECTORS

3.1 FRONT PANEL

A general view of the TEC f ront panels is given in Fig. 3. The users elem ents will be described starting from bottom to top from the lower right to left. The current clam p controls and those for adjustment of the current signal are oriented on the right side of the front panel. The keys for the digital control units and the digital display are found in the m iddle, and the elements for the voltage clamp mode are oriented on the left side of the panel.

HEADSTAGE INPUTS: Plugs for connection of the headstages

POTENTIAL ELECTRODE C. COMP.: Capacity ne utralization (potential electrode, current electrode (optional))

POTENTIAL/CURRENT ELECTRODE OFFSET: Offs et compensation (potential electrode, current electrode)

CURRENT HEADSTAGE BIAS: Zero setting for the current source (current electrode)

CURRENT OUTPUT FROM HEADSTAGE: Current signal from the current headstage (0.1V / μA

CURRENT OUTPUT FILTERED: Current output (signal passed through transient compensation (TEC-10CX only), current sensitivity selection amplifier and low-pass filter) CURRENT TRANSIENT COMPENSATION (TEC 10CX only;): Capacitive transient (A1,

A2, A3, T1, T2, T3) and leakag e current (LINEAR) compensation in voltage clam p mode. BYPASS / ON. switch. If set to BYPASS the current transient compensation unit is switched off, i.e. am plitude and tim e constant settings of the transient compensation unit are not working. If set to ON the current transien t compensation unit is active. The LINEAR component is not affected by this switch.

<u>Caution</u>: In current clam p m ode, the a1-a3 and LINEAR controls m ust be in the zero position.

BATH POTENTIAL (mV) (TEC 05X only): signal at the REF electrode

CURRENT FILTER (Hz): Current output low-pass filter

CURRENT OUTPUT SENSITIVITY: Am plification switch f or the CURRENT OUTPUT signal ($0.1V/\mu A - 10 V/\mu A$ in seven steps)

HOLDING CURRENT: +/0/- switch: Holding current control (current clamp mode)

CURRENT STIMULUS INPUT: Current stimulus input in current clamp mode

MODE OF OPERATION (TEC-10CX): Control unit for selection of the operation mode:

 $R_{Cel}\,$ - Resistance test of current electrode

CC - Current clamp mode

VC - Voltage clamp mode

R_{Pel} - Resistance test of potential electrode

MODE OF OPERATION (TEC-05X): Control unit for selection of the operation mode:

BRIDGE - Bridge mode for potential electrode

CC - Current clamp mode

OFF - In this position the amplifier does not apply any voltage or current to the cell

VC - Voltage clamp mode

EXTERN: if this position is selected, the mode of operation can be set by a TTL pulse applied to the MODE SELECT INPUT (TTL) BNC; LO = CC, HI = VC or DHC (if installed). DHC – Dynamic Hybrid Clamp mode (option)

DIGITAL DISPLAYS (3 ¹/₂ digits):

POTENTIAL/RESISTANCE: membrane potential in mV, resistance in M Ω , with LED's for the selected unit (mV, M Ω):

CURRENT: current in μA

POTENTIAL: Switch for upper digital display

Electrode switch:

POTENTIAL ELECTRODE - Membrane potential or resistance of the potential electrode CURRENT ELECTRODE - Potential of the current electrode or resistance of the current electrode

OSCILLATION SHUT-OFF: Protection against oscillation of the amplifier

THRESHOLD: Adjustment of the threshold

DISABLED/RESET: Choice of operation, DISABLED = unit is not active.

LED: red: amplifier shut-off; green: turned on/in operation

HOLDING POTENTIAL (mV), -/0/+ switch: Holding potential control in VC mode VOLTAGE COMMAND INPUT: Command signal in voltage clamp mode (:10 mV or :40 mV)

POTENTIAL OUTPUT P_{EL} : (x10 mV or x40 mV): Membrane potential output POTENTIAL OUTPUT C_{EL} : (x10 mV): Potential output of current electrode

COMMAND FILTER TIME CONSTANT: Tim e constant of the filter for the VOLTAGE COMMAND INPUT (10 μ s to 1 ms)

INTEGRATOR TIME CONSTANT - ON/FF switch: Ti me constant of the integrator of the proportional-integral controller (PI controller)

VC OUTPUT LIMITER (0-100%): Limits maximum current in voltage clamp mode

VC GAIN: Proportional amplification of the PI controller

VC ERROR: Display of control error

SERIES RESISTANCE COMPENSATION / ON switc h: Adjustm ent of series resistance compensation (cur. prop. amplification)

AUDIO MONITOR: Monitors the potential signals

POWER: Power switch

TEC-05X option

Optionally the TEC-05X is equipped with a BUZZ function to facilitate penetration of the cell membrane. BUZZ operation is based on overcom pensation of the respective electrode. The electrode for BUZZING is selected via the ELECTRODE RESISTANCE switch.

BUZZ: Push button to activate BUZZ

DURATION: Potentiometer to set the duration of the BUZZ.

TTL REMOTE: BNC connector for connecting a remote switch. TTL high = BUZZ

3.2. REAR PANEL

FUSE (0.63 A/220V, 1.25 A/110V, SLOW): Mains fuse 115/220V AC: Mains cable hook-up through an IEC standard plug INTERNAL GROUND: System ground PROTECTIVE EARTH: Mains ground CURRENT OUTPUT SENSITIVITY MONITOR (1V/ STEP; +1 to +7V): signal m onitoring the position of the CURRENT OUTPUT SENSITIVITY switch CURRENT FILTER MONITOR (1V/STEP, -7 to +8V): signal monitoring the position of the CURRENT FILTER switch

3.3. POTENTIAL HEADSTAGE

P_{EL}: Electrode connector with "driven shield" REF: Connection of the reference electrode (for measurement of the bath potential) GND: Ground connector

TEC-05X option

The TEC-05X is equipped with a bridge m ode. With the electrode connected to the potential headstage the user can penetrate a cell, m easure membrane potential and apply current pulses in CC mode. In BRIDGE mode (see MODE OF OPERATION switch) the TEC-05X operates like a single electrode bridge amplifier (see also separate manual).

3.4. CURRENT HEADSTAGE WARNING!! LETHAL HIGH VOLTAGE

CURR. EL.: Connection for the current electrode

GND: Ground connector

RANGE (x1/x0.1 or x0.1, x1, x2, x5 or x0.1, x0.2, x0.5, x1) : Selection of the current range (option)

BANDWIDTH (wb/10 Hz): Selection of the bandwidth (see 2.5, Low Noise Mode) **WARNING:** If the bandwidth of the current headstage is set to 10 Hz, som e functions such as R_{Cel} (current electrode resistance test) do not work properly.

4 INSTALLATION

4.1. GENERAL CONSIDERATIONS

Safety

IMPORTANT: Please f ollow strictly all regulati ons outlined in chapter 0 (SAFETY REGULATIONS.

In working with the <u>TURBO TEC</u> systems, always adhere to the appropriate safety m easures for handling electronic devices. This instrum ent functions with a high voltage outlet (CUR.

EL. plug on the current headstage). After turning on this instrument, it must be ensured that the interior contact of this plug cannot be touc hed. In addition, it is extrem ely important that the instrument is turned **off** when changing or adjusting either electrode!

In addition, both headstages contain very se nsitive FET am plifiers, which can be dam aged with electrostatic charge, and must therefore be handled with care.

Turn power off if headstages are connected or disconnected from the connectors on the front panel of the 19" cabinet.

TEC systems shall be used only in a warmed-up condition to avoid temperature related errors. Before using the TEC systems the output bias current of the current injection headstage m ust be canceled. The tuning procedure is describe d in chapter 4 / INSTALLATION (Adjustm ent of the Current Offset (CURR. OUTPUT OFFSET)).

Prevention of Line Interference

It is recommended that all experiment should be carried out in a shielded environment (Faraday cage, connected to ground). Such systems are described in the literature (e.g. see refs. 1, 10 and 17). All components inside this shielded area such as microscopes, micromanipulators etc. must also be grounded properly. All electric systems in this area such as lamps, perfusion valves, electrical micromanipulators etc. Are sources of noise which may deteriorate the measurement.

All <u>TURBO TEC</u> instrum ents have a high quality toroid transf ormer to keep down stray fields. In spite of this, noise problem s could occur if other m ains-operated instruments are used. The internal system ground (GND socket) should be connected to only one point on the measuring ground, and should originate from the potential headstage. The casings of the headstages are grounded and could be used (which m ay form so-called "ground loops") should be avoided; all ground points should originate from a central point.

IMPORTANT: The system ground can be disconnected or connected to the m ains ground on the back of the instrument.

4.2. TESTS AND TUNING PROCEDURES

General Considerations

The amplifier must be in the current clamp mode when first turned on.

All system s need a warm -up period of about 20-30 m inutes. The instrum ents should be calibrated and used for measurements only after this time.

All sym metrical offset adjustment have the zero position at 5.00 on the respective scale. Before turning on the instrument, all offset controls should be set approximately at this position, and all other controls and adjustments should be at zero.

All systems based on feedback circuits such as capacity compensation controls or voltage clamp gain must be on a low position (close to zero) when starting the tuning procedure. The INTEGRATOR part of the PI controller must be set OFF before switching to VC mode.

Basic connections

The basic connections for testing and using a TEC system s are given in figs. 4-6. The minimum equipment needed is a stim ulus unit and an oscilloscope (preferable digital storage scope). Usually a com puter based data acquisiti on system is used for experiments. This is connected in a similar manor (i.e. ADC = analog-to-digital converter to the outputs and DAC = digital-to-analog converter to the inputs of the TEC system).

For a detailed description of basic set-up construction and necessary equipment please consult the literature (Chapter 6, refs. 1, 10, 11, and 21-23).

The stimulator or DAC output of the data acquisition system is connected either to the one of the CURRENT STIMULUS INPUT BNCs or the VOLTAGE COMMAND BNCs.

Two input channels of the scope or ADC inputs of the data acquisition system are connected to the CURRENT OUTPUT respectively POTENTIAL OUTPUT of the TEC.

For remote operation through TTL signals or from the computer system connect TTL signals or control cable to the MODE of OP ERATION selection BNCs (see 2.7, DIGITAL CONTROL UNIT).

Connect headstages to electrodes or cell model as outlined in figs. 5 and 6.

TEC Cell Model

The testing of the <u>TURBO TEC</u> systems should be perform ed with appropriate cell m odels (see Fig. 5 and Fig. 6). For all tests ex cept the CURRENT HEADSTAGE BIAS TUNING procedure the TEC Cell Model must be set "ON".

We recommend the 100k m embrane resistance (see Fi g. 6), and the use of square test pulses of 1 V. This corresponds to a command of 1 μ A in CC m ode and to 100 m V steps in VC mode.

Adjustment of the CURRENT HEADSTAGE BIAS CURRENT control

This tuning procedure is very important since it determines the accuracy of the TEC system. TEC system s are equipped with a high-voltage current source which is connected to the current injecting electrode and perform s the current injection (see SYSTEM DESCRIPTION chapter, 2.2). This current source has a high-impedance floating output. Therefore the zero position (i.e. the zero of the bias current) of this device has to be defined.

Since the used high-voltage FET am plifiers become worm from the internal heat dissipation and their characteristics are strongly tem perature dependent, the calibration procedure has to be done periodically by the user. The tuning procedure is done using the C. HEADSTAGE BIAS CURRENT control and a resistance of a few M Ω . It is based on Ohm's Law: the voltage deflection caused by the output current generated by the headstage on a test resission is displayed on the digital meter. The output current which is proportional to the monitored voltage deflection is nulled with the C.HEADSTAGE BIAS CURRENT control.

This tuning procedure cannot be performed with an electrode since there always are unknown offset voltages involved (tip potential, junction pot entials etc.). Therefore a test resistor of 1-10 M Ω m ust be used. If the TEC Cell m odel is used (see Fig. 6) only the C _{EL} and GND (=ground) connectors m ust be connected. The "ON/OFF(GND) switch can be used for the nulling procedure described below.

First, the connection to the current electrode must be grounded. This can be done directly with the wire connected to the C $_{EL}$ connector or if the TEC Cell Model is used, by switching the cell model in the OFF (GND) position.

Now the offset potential of the CURRENT ELECTRODE POTENTIAL output can be nulled. Therefore the digital display is switched to the potential output of the current electrode.

(SELECT switch to the left of the upper digital disp lay) and the display is set to zero with the potentiometer C_{EL} OFFSET.

Next, a resistance of 1-10 M Ω is connected from the current headstage output to ground (as if an electrode were attached), or the cell model is switched in the "ON" position.

The digital display (and the CURRENT ELECTRODE potential connector (C_{EL} POTENTIAL OUTPUT /x10m V)) now show a voltage deflec tion which is proportional to the flowing output current.

The output bias current can be tuned to zero with the C. HEADSTAGE BIAS CURRENT control. The current is zero when the voltage deflection is zero.

As a rule, the current outputs (CURR. OUTPUT UNCOMPENSATED, CURR. OUTPUT) and the CURRENT DISPLAY (lower digital display) should also read 0 µA.

This calibration can also be m ade during an experiment, since no electrode is necessary and the potential reading is not affected.

Test of the Current Clamp Mode

First, the appropriate cell m odel is set up, followed by the zero-adjustm ent described above. After this, the offset of the potential electrode is set at zero. If this adjustment is not possible, it is an indication that the input am plifier in the headstage has been dam aged by electrostatic charge.

After the offset adjustment, the function of the current clamp can be tested. By application of a holding potential of, for exam ple, 1 μ A, the membrane resistance m ust result in an appropriate change in potential (according to Ohm's law).

Test of the Voltage Clamp Mode

After the test of the current clam p mode, the function can be switched to the voltage clam p mode. The control parameters are best adjusted by application of a te st pulse. (see Appendix 2, Empirical Tuning Procedure).

After this, the current is measured relative to a given holding potential (for example 100 mV). The measured current should correspond with one calculated by Ohm's law.

Testing of the Zero Current / Zero Potential

After completing these three tests, all input signa ls should be shut off. All potentials and the current should equal zero in both the current clamp and in the voltage clamp mode.

Resistance Measurement

Test the resistances of the m icroelectrodes by first switching the MODE OF OPERATION key to the appropriate test system (TEC-10CX) or by switching the ELECTRODE RESISTANCE switch to the desired position.

The function of this system is tested with the most accurate resistances possible. These systems function independently of the other adju stments, with the condition that a connection between the electrodes and ground connector (bath) exists.

If an appropriate display does not appear relative to a given resistance, it is an indication that the input amplifier of the respective m easuring headstage has been dam aged by electrostatic charge.

Adjustment and Test of the Transient Compensation (TEC 10CX)

This test should be perform ed after com pletion of the voltage clam p m ode test. W ith the application of test pulses, it m ust be possible to suppress the capacitive current to a large extent with the regulators A1-A3 and T1-T3.

The linear "leakage" current which flows through the resistance of the cell m odel must be compensated by the "LINEAR" potentiometer.

Note that the BYPASS / ON switch has to be in ON position in order to use the transient compensation.

Oscillation Shut-Off

First, set the DISABLED/RESET switch in the DISABLED position (green light will light). Then set the switch in the m iddle position, a nd overdrive the C-com pensation to cause oscillations to appear.

The response threshold can now be adjusted with the THRESHOLD potentiom eter. If the system responds, the LED will light red, and the current injection and C-com shut off.

In order to adjust the C-com pensation to normal, the system can be restored by switching to the RESET position.

After successful com pletion of these adjustm ents, the instrum ent is ready for use. For experimental measurement, follow the sam e order of procedure: adjustm ent of the offset compensation in the bath, pre-adjustm ent of the C-compensation and of the shut-off, and further adjustments after the positioning of the electrodes.

5 CONTROL THEORY APPENDIX

5.1 THEORY OF OPERATION OF THE TEC SERIES AMPLIFIERS

The standard configuration for voltage clam ping oocytes is the two electrode voltage clam p arrangement (19-23). In contrast to previously described clamp systems (for review see ref. 11 and 20) the instruments for oocyte clamping must meet special requirements since oocytes are very large cells with a high m embrane capacity (up to 100-500 nF) and large m embrane currents (up to 100 μ A and more).

Voltage clamp instruments are closed loop cont rol systems with two inputs which act from outside on the control loop. An electronic feedback network is used to force the membrane potential of a cell to follow a voltage command (setpoint input) as fast and as accurately as possible in the presence of incoming disturbances (disturbance input, correlated with the activities of the cell) by injecting an adequate amount of charge. The current injected by the clamp instrument is a direct measure for the ionic fluxes across the membrane. (see references 4, 9, 11 and 20).

The performance evaluation and optim al tuning of the system can be done by considering only the command input since the m athematical models (set point transfer function and the disturbance transfer function, see 5 and 10-13) are closely related. Modern control theory provides adequate solutions for the design and optimal tuning of feedback systems (5).

Most voltage clamp systems are composed only of delay elements, i.e. elements which react with a retardation to a change. This type of closed loop system s can be optim ized easily by adequate shaping of the "frequency characteristic m agnitude" (/F(jw)/) of the associated transfer function F(s) (output to input ratio in the frequency dom ain = LAPLACE transform of the differential equation of the system).

Using controllers with a proportional-integral characteristic (PI-controllers) it is possible to force the magnitude of the frequency characteris tic to be as close as possible to one over a wide frequency range ("modulus hugging", see 5, and 12-15). This m eans that the controlled membrane potential rapidly reaches the desired command value.

The PI controller yields an instantaneously fast response to changes (proportional gain) while the integral part increases the accuracy by raisi ng the gain below the corner frequency of the integrator (i.e. for slow signals) to very high values (theoretically to infinite for DC signals, i.e. an error of 0%) without affecting the noise level and stability. Since the integrator induces a zero in the transfer function, the clamp system will tend to overshoot if a step command is used. Therefore the tuning of the controller is performed following optimization rules which yield a well defined system performance (AVO and SO, see below).

The various components of the clamp feedback electronics can be described as first or second order delay elements with time constants in the range of m icroseconds. The cell capacity can be treated as an integrating element with a time constant Tm which is always in the range of hundreds of milliseconds.

In comparison with this "physiological" tim e constant the "electronic" tim e constants of the feedback loop can be considered as "sm all" and added to an equivalent tim e constant Te. The ratio of the "small" and the "large" time constant determines the maximum gain which can be achieved without oscillations and thus the accuracy of the clam p. With the gain adjusted to this level the integrator tim e constant and "small" time constant determine the speed of response of the system.

Tuning of the clam p is perform ed according to optim ization rules: The "absolute value optimum" (AVO) provides the fastest response to a command step with very little overshoot (maximum 4%) while the "symmetrical op timum" (SO) has the best perform ance compensating intrinsic disturbance signals . The SO shows a considerable overshoot (maximum 43%) to a step command, which can be reduced by adequate shaping of the command pulse by a delay unit (5 and 13). An empirical tuning procedure is given in APPENDIX 2.

The upper speed lim it is determ ined by the m aximum am ount of current which the clam p system can force through a given electrode (see APPENDIX 3). The clam p performance can be increased considerably if the influence of the current injecting electrode is excluded as far as possible from the clamp loop since the electrode resistance is nonlinear. This is achieved if the output of the clamp system is a current source rather than a voltage source; in this case the clamp transfer function has the m agnitude of a conductance (A/V). Other advantages of this arrangement are that the clam p current can be determ ined by a dif ferential am plifier (no virtual ground is needed, (see 6, 13) and that th e bandwidth of the feedback system can be altered easily (e.g. for noise suppression during simultaneous patch clamp recordings, see 19-21).

This output circuit m ust be equipped with large bandwidth high voltage operational amplifiers. To avoid deterioration of clam p perform ance caused by electrode overload the output current has to be lim ited by an electronic circuit to a safe level. With electrodes in the range of one M Ω and a voltage of ±150 V the m aximum current will be 150 μ A. W ith this current a cell with a capacity of 0.1 μ F can be depolarized by 100 m V in approxim ately 100 μ s, which com es close to the theoretically possible speed of response, without any detectable deviations from the command level. With an output compliance of 225 V and a x2 or x5 range current injecting headstage currents up to 500 μ A can be injected (see 6 and 15).

The speed of response and the accuracy of a two electrode clamp system is determined by the cell capacity, the resistance of the current in jecting m icroelectrode (which lim its the maximum amount of injected current) and the e quivalent time constant and accuracy of the potential recording and feedback electronic sy stems. Therefore the design of the potential recording site is very important. A differential potential registration with a reference electrode which registers the bath potential m inimizes errors due to resistances in series with the cell membrane. Driven shield and capacity compensation circuits are used to improve the speed of response.

In some cases, a series resistance compensation circuit which adds a current proportional gain can improve the clamp performance considerably (6). The use of such a circuit enhances the speed of response and im proves the accuracy of the clam p system. Since both circuits are positive feedback loops the noise level is also increased.

In addition to the elem ents of the clam p loop itself the oocyte clam p amplifier needs som e additional units which f acilitate the work such as electrode resistance test units, oscillation shut-off unit, adequate output signal am plification, filtering and display units, facility for compensating the capacitive currents, etc.

EMPIRICAL TUNING PROCEDURE FOR PI CONTROLLERS

Before switching to voltage clam p mode all pa rameters related to the recording electrodes (offset, capacity compensation etc.) have to be tuned in CC m ode. With PI controller based clamps capacity compensation tuning can be rep eated whenever it is necessary, also in VC mode.

Before switching to VC mode gain control has to be reduced to a safe level, integrator has to be switched off to increase stability.

The PI controller is first used as P controller only (INTEGRATOR switch in "OFF" position). The command input is used without sm oothing. Identical command pulses are applied. The gain is increased until the overshoot of the de sired tuning method appears. Using only the P part of the controller means that a steady-state error will be present.

Now the I section is reconnected to form the PI controller (INTEGRATOR in "ON" position). The integrator time constant is set to give the desired overshoot according the optime ization rules of Appendix 1.

If the SO is used, an external command input filter has to be used to reduce the overshoot according to the requirements of the experiment.

5.3 SPEED OF RESPONSE AND LINEARITY OF THE CAPACITIVE TRANSIENTS

For the investigation of voltage activated ch annels with voltage clam p instrum ents som e special techniques for eliminating the capacitive and leak currents have been introduced, such as the P/4 ore more general P/N protocol (see 17 for overview). For these protocols the speed and linearity of response of the clamp system is of great importance.

As outlined in APPENDIX 1 the TEC system s are designed f ollowing a control theory procedure called "m odulus hugging" (see referen ces 5, 12-16). The procedure requires a PI (proportional-integral) controller. This procedure is applicable to control system s composed of an element with one "large" time constant and many "small" time constants. These "small" time constants can be added to an "equivalent" time constant Te.

In case of the TEC control chain the "large" time constant is formed by the cell membrane (several hundred of membranes) and the sum of "smeall" time constants results from the microelectrodes and the electronics (a few ten μ s). Here we consider only the proportional part of the PI controller. We also do not consider possible improvement of clamp performance due to series resistance compensation 6-8, and 20 for details).

General Considerations

For the TEC system s the "sm all" time constants are at least two orders of m agnitude below the "large" time constant:

The "large" time constant is the time constant of the membrane and the equivalent time constant is composed of the time constants of the electrodes, amplifiers etc.

 $T_m = R_m * C_m, T_e = \Sigma T_i$

The performance of a clamp system can be improved if a voltage controlled current source is used for the current injecting electrode. In this case the very large time constant (hundreds of milliseconds) formed by the electrode resistance and the cell capacity can be ignored since the output of the clamperized performance of the flows regardless of the resistance of the injecting microelectrode (see reference 20 for de tails). Thus the performance of the clamperise is not longer depending on the electrode resistance can be called as the current source is not saturated). The clamperise pain in this case has the magnitude of a conductance [A/V].

The proportional gain of the clamp system can be calculated as follows (references 5 and 12):

$K = C_m / 4T_e$	Linear optimum (LO), aperiodic response, no overshoot
$K = C_m/2T_e$	Modulus optimum (MO), 4% overshoot, fastest rise time

The gain which should be used in a VC e xperiment is between these two values. The overshoot can be reduced by low-pass filtering of the command pulse.

The speed of response of the clamp in case of the modulus optimum can be calculated as:

 $T_r = 4.7 T_e, T_s = 8.4 T_e$

 T_r - time until the membrane potential reaches for the first time 100% of the command pulse T_s - time to reach steady state within a tolerance of 2%.T_s is roughly the duration of the capacitive transient. For a system with dampened overshoot T_r approaches T_s .

From these form ulas it is clear that the performance of the clamp is determined by T_e . T_e is determined by the time constant of the current injecting electrode i.e. by the electrode resistance, stray capacities, cable capacities etempined cables have capacities of 60-110 pF/m, connectors and pipette holders add a equipped with a driven shield and a capacity constant is always much smaller than the time constant associated with the current electrode. The time constants of the operational amplifiers can be neglected.

Example

A cable of ca. 10 cm has a capacity of ca. 10 pF, with the stray capacities in the headstage and an electrode resistance of 1 M Ω (cell m odel) this gives a tim e constant of 10-30µs (corner frequencies of 5-15kHz). With Cm = 0.1µF and Te = 20µs (8kHz bandwidth) the gain can be calculated as:

LO: K = 1.25 mA/V MO: K = 2.5 mA/V

The standard TEC current source has a calibration of 10 μ A/V. This means that the gain stages related to the GAIN control on the front panel must provide a gain between 125-250. In the TEC system the gain amplifier is composed of two stages: x10 fix and 1-100 variable. The maximum gain of the variable gain stage can be set with an internal trim potentiometer. If a pulse of 150 m V is applied, the output of the first stage is 1.5V while the second stage goes into saturation if the calculated gain values are used. Therefore the capacitive transients will have large nonlinear components.

A response with no saturation effects is obt ainable only with com mand signals below 100 mV. If larger m embrane capacities are used in the cell m odel, the saturation effects start earlier, because in this case a higher gain is required. To improve this behavior systems with higher output com pliance and/or headstages w ith x2, x5 or x10 ranges m ust be used, which avoid the saturation effect of the gain amplifier (see references 6), 13), and 14)).

The speed of response (with x1 headstage and 150 V output) from the point of view of control theory is: $T_r = 94\mu s$ and $T_s = 168\mu s$.

Maximum speed of response

The speed of an ideal VC system is limited only by the maximum current delivered by the current source:

 $\label{eq:constraint} \begin{array}{l} [dUm/dt]max = Umax/(C_m * R_{EL}) \\ [dUm/dt]max = 150V/(0.1 \ \mu F * 1 \ M\Omega) = 1500 \ V/s = 1.5 \ mV/\mu s \\ To reach 150 \ mV \ would \ last 100 \ \mu s \ provided \ that \ the \ clamp \ has \ an \ ideal \ characteristic. \end{array}$

Now we can calculate the m inimum bandwidth of a real clam p system necessary for "ideal" behavior:

 $T_s = 8.4 T_e = 100 \ \mu s$ gives $T_e = 12 \ \mu s$; BW = $1/(2\pi^*T_e) = 13 \ kHz$ If we assume that Te is determ ined by 70-80% by the time constant of the current electrode (i.e. $T_{el} = 10\mu s$ if $T_e = 12\mu s$) it is clear that with electrode resistances in the range of 500 k Ω the total capacity related to the current injecting electrode can be maximum 20pF. Maximum cable length in this case is 15-20 cm.

A cable of 0.5-1.5 m has a capacity in the range of 50-200 pF. W ith such a capacity and an electrode resistance of 1 M Ω T_e is in the range of 50-200 µs and the speed of response would be in a range of 0.5 - 2 ms!

Conclusions:

For adequate VC experim ents a clam p gain of 1-5 m A/V (i.e. 100-500 internal gain with a current source calibration of 10 μ A/V) is nece ssary. Therefore with pulse am plitudes of 100-200 m V the operational am plifiers in the gain stages will be saturated causing nonlinear components in the capacitive transients.

The maximum speed of response is determ ined by the cell capacity, the m aximum available current and the command amplitude.

The real speed of response is determ ined by the time constant associated with the current injecting electrode. It is strongly dependent on the length of the used cable.

The speed of response and the linearity of the capacitive transients can be improved considerably if a current headstage with a steeper gain ($x^2 = 20 \ \mu A/V$, $x^5 = 50 \ \mu A/V$) is used especially in combination with a higher output voltage of $\pm 225 \ V$ (TEC 220 System) and an improved series resistance compensation (see references 2, 6 and 13 and 15).

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7 TURBO TEC SERIES SPECIFICATIONS

All following current signal related param eters are for the TEC 05 and TEC 10 instrum ents with standard 150 V current headstage

Parameters for the other system s or for syst ems with a selectable current ranges can be calculated from these parameters.

MODES of OPERATION

DHC Dynam ic Hybrid Clam p Mode (TEC-05 option); CC Current Clam p Mode; VC Voltage Clamp Mode; OFF Mode; BRIDGE Bridge Mode (option TEC-05) MODE selection: 6-position toggle switch (TEC 05), or pushbuttons (TEC 10), LED indicators; remote selection by TTL inputs.

ELECTRODE RESISTANCE test

POTENTIAL ELECTRODE: m easurement of the ELECTRODE RESISTANCE of the POTENTIAL ELECTRODE CURRENT ELECTRODE: m easurement of the ELECTRODE RESISTANCE of the CURRENT ELECTRODE

HEADSTAGES (TEC-10)

Potential headstage:

Differential input (for suppression of bath potentials), cmr > 80 dB; Input resistance: >10¹³ Ω ; operating voltage ±15 V.

Electrode connector: BNC with driven shield; driven shield range: ± 15 V, output im pedance 250 Ω .

Reference connector (bath) gold-plated SUBCLIC, grounded shield; ground connector: 2.3 mm connector or headstage enclosure.

Size: 65x25x25 mm, headstage enclosure is connected to ground. Holding bar: diameter 8 mm, length 10 cm.

Current headstage (high voltage):

Operating voltage range : ± 150 V (standard, TEC-10) or ± 225 V (TEC 225 system s); input resistance: >10¹² Ω (internally trim mable); electrode connector: gold-plated SUBVIS connector, grounded shield.

Power dissipation: 6 W (standard system) or 20 W (TEC 225 system).

Size: 100x50x30 m m with heat sink 100x50x20 (150V) or 225x40x60 m m or equivalent (TEC 225), grounded enclosure; holding bar (iso lated from ground, standard system only): diameter 8 mm, length 10 cm.

 $\begin{array}{ll} Current \ range: \\ 150 \ \mu A \ / \ 1 \ M\Omega \ (TEC \ 10CX) \ oocyte \ systems) \\ 220 \ \mu A \ / \ 1 \ M\Omega \ (TEC \ 225) \\ Current \ range \ switch \ (optional): \\ & \quad -x2, \ x5, \ x10, \ or \ x0.1; \ -x0.1, \ x0.2, \ x0.5, \ x1 \end{array}$

Current headstage (TEC-05): see additional information sheets

Bandwidth and Speed of Response:

Full power bandwidth (Re = 0) :> 100 kHz; rise tim e (10-90%, current pulse of 100 μ A applied to Re = 1 MΩ) < 30 μ s;

Bandwidth switch: wide band or 10 Hz for parallel patch clamp recordings

Current Electrode Parameter Controls:

Leakage current: adjustable to zero with ten-turn control; of fset com pensation: ten-turn control, ± 1200 m V; capacity com pensation (optional, TEC 05X range 0-30 pF, ten turn potentiometer.

Potential Electrode Parameter Controls:

Capacity compensation: range 0-30 pF, ten turn control; offset compensation: ± 200 mV, tenturn control.

POTENTIAL OUTPUTS:

Potential electrode: two outputs, sensitiv ity x10 m V and x40 m V, output impedance 250 Ω ; output voltage range ±15 V.

Current electrode: sensitivity x10 mV; output impedance 250 Ω , output voltage range ±15 V. DISPLAY (switch selected): XXX mV.

AUDIO MONITOR:

Pitch correlated with potential signals, switch selected.

OSCILLATION SHUT-OFF:

Turns off current injection and capacity com pensation, function displayed by red/green LED, disabled /off/reset switch, threshold set with linear control (0-1200 mV).

ELECTRODE RESISTANCE TEST (both electrodes):

100 mV / M Ω , obtained by application of square current pulses ±10 nA, display XX.X M Ω , selected automatically.

CURRENT OUTPUTS:

Uncompensated output signal: sensitiv ity 0.1 V/ μ A, output resistance 250 Ω , output voltage range ±15 V.

Compensated /filtered output: sensitivity: $0.1...10 \text{ V/}\mu\text{A}$ in 1-2-5 steps, selected by rotary switch, with lowpass Bessel filter, output impedance 250 Ω ; sensitivity monitor: 1...+7 V, 1V / switch position, output impedance 250 Ω , DISPLAY: X.XX μA .

CURRENT SIGNAL PROCESSING:

TEC 10: transient compensation unit with three overlapping ranges, (max: T1 = 3.3 ms, $T2 = 330 \mu \text{s}$, $T3 = 33 \mu \text{s}$), time constants set with ten-turn controls, amplitudes set with one-turn linear controls, leakage compensation maximum 1 μ A.

CURRENT OUTPUT FILTERS:

TEC 05X: two-pole (standard version) or f system) with 16 corner frequencies, 20 Hz - switch position, output impedance 250 Ω . our-pole lowpass Bessel f ilter (TEC 05X-BF 20 kHz; frequency m onitor: -8...+7 V, 1 V /

TEC 10CX: f our-pole lowpass Bessel f ilter with 16 corner f requencies, 20 Hz - 20 kHz; frequency monitor: -8...+7 V, 1 V / switch position, output impedance 250 Ω .

CURRENT CLAMP (TEC 10, standard current headstage):

Inputs: 1 μ A/V, 0.1 μ A/V with ON/O FF switches, input resistance > 100 k Ω : HOLD: X.XX nA, ten-turn digital control with -/0/+ switch, maximum 10 μ A.

For TEC-05X see separate sheets

Noise: potential output: $100 \ \mu V \ pp$; current output 200 pA pp with 1 M Ω resistance and 10 kHz bandwidth (internal four-pole Bessel filters).

Speed of response (1% settling times; potential output signals after application of square pulses of 1V with 1 M Ω electrode resistance): potential electrode < 10 μ s, current electrode < 50 μ s.

VOLTAGE CLAMP:

Inputs: two inputs with ON/OFF switches, sens itivity :10 m V and :40 m V, input resistance > 100 k Ω ; HOLD: XXX mV, ten-turn digital control with +/0/- switch, maximum 1000 mV. RISE TIME LIMIT: 0-0.2 ms

GAIN: 10 μ A/V - 10000 μ A/V ,ten-turn linear control.

INTEGRATOR TIME CONSTANT: 200 µs - 2 ms, ten-turn control.

OUTPUT CURRENT LIMIT: 0-100% linear control.

NOISE (filters set to 10 kHz, other settings see below)

Potential output: $< 100 \ \mu$ V pp, current output: $< 10 \ n$ A pp at 10 kHz, $< 2 \ n$ A at 500 Hz

SPEED of RESPONSE (VC Mode):

1 % settling time: < 80 μ s for 10 mV step and < 100 μ s for 100 mV step applied to cell model (R_{EL} = 1 M Ω , R_m = 100 k Ω , Cm = 0.1 μ F, standard headstage); < 80 μ s with 225 V headstage.

POWER REQUIREMENTS: 115/230 V AC, 60 W (1.25/0.63 A fuse, SLOW)

DIMENSIONS:

19" rackm ount cabinet, 19" (483 m m) wi de, 14" (355 m m) deep, 5.25"(132.5 m m) high, weight 11 kg

ACCESSORIES PROVIDED:

Potential headstage, standard current headst age (other headstages m ay be substituted on request with order) Cable set and connectors for reference, current electrode, and ground connectors Power cable Operation manual.

OPTIONAL ACCESSORIES (ordered at additional cost):

TEC-MOD: passive model cell; -ODA: active model cell. High voltage headstage with four current ranges. TEC-EH-SET: electrode holder set TRC-01M (TEC 05X system s only): Current transient com pensation m odule for TEC 05 amplifiers.

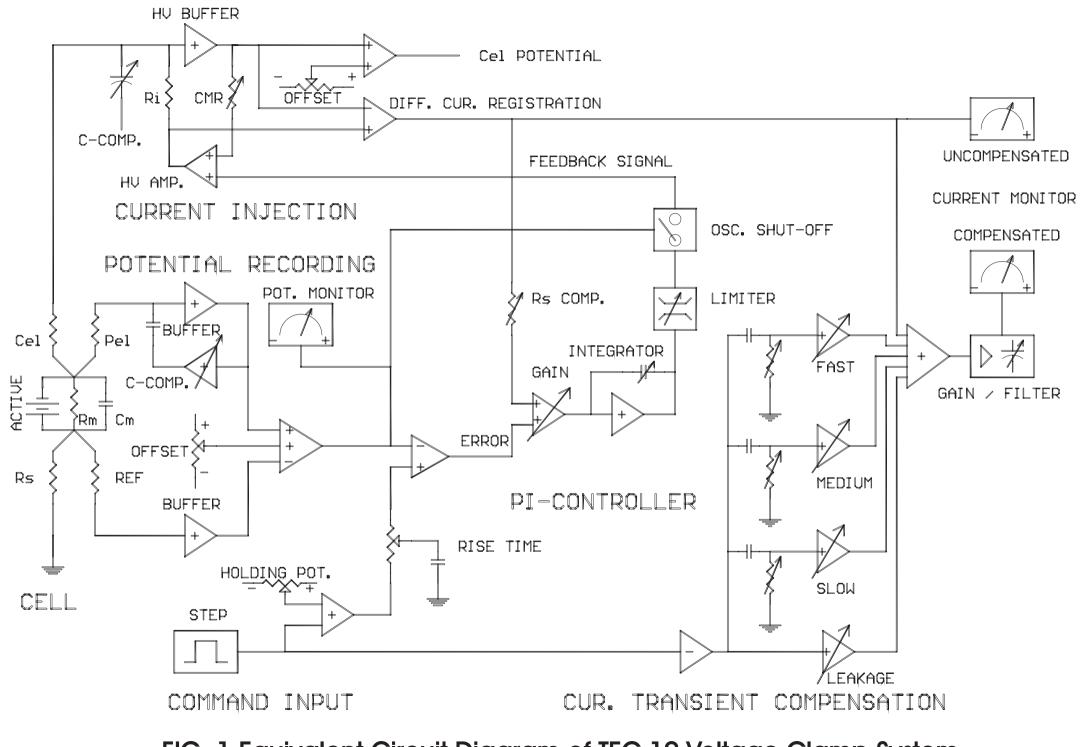


FIG. 1 Equivalent Circuit Diagram of TEC 10 Voltage Clamp System

Fig.2B BLOCK DIAGRAM OF VC MODE

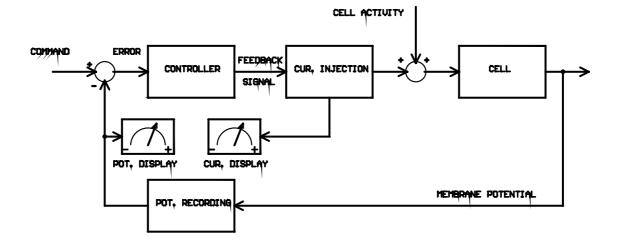


Fig.2A EQUIVALENT CIRCUIT OF TEC AMPLIFIER

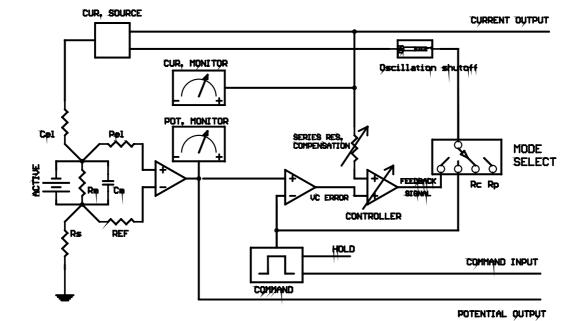




Fig. 3: TEC-10CX Front Panel

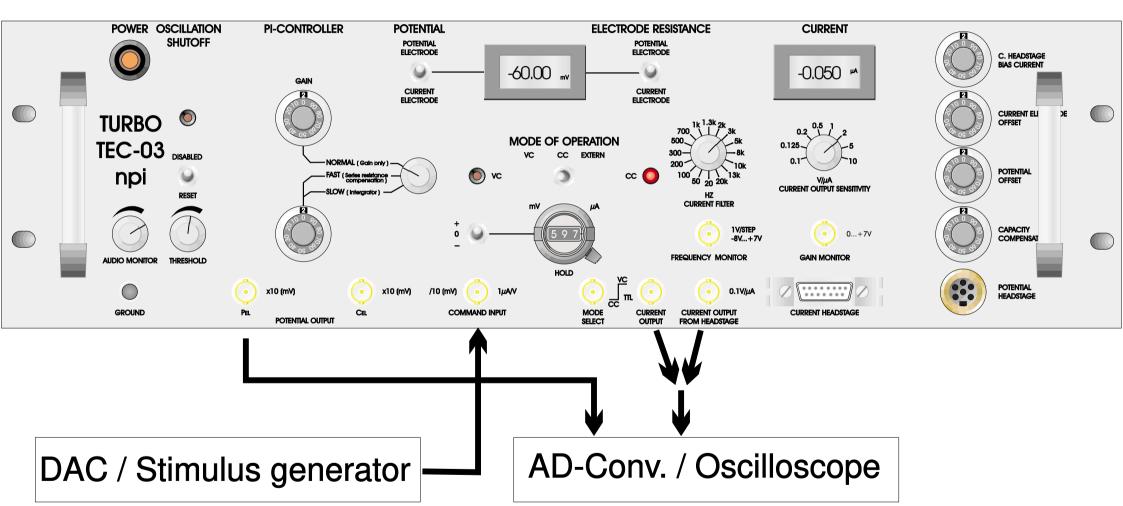


FIG. 4 CONNECTIONS

FIG.5 HEADSTAGE CONNECTIONS

