

Made to measure

OPERATING INSTRUCTIONS AND SYSTEM DESCRIPTION OF THE

SEC-05X

SINGLE-ELECTRODE CLAMP AMPLIFIER



VERSION 2.0
npi 2015

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About this Manual

This manual should help the user to setup and use SEC systems correctly and to perform reliable experiments.

If you are not familiar with the use of instruments for intracellular recording of electrical signals please read the manual completely. The experienced user should read at least chapters 1, 3, 7 and 10.

Important: Please read chapter 1 carefully! It contains general information about the safety regulations and how to handle highly sensitive electronic instruments.

Signs and conventions

In this manual all elements of the front panel are written in CAPITAL LETTERS as they appear on the front panel.

System components that are shipped in the standard configuration are marked with ✓, optional components with ⇄. In some chapters the user is guided step by step through a certain procedure. These steps are marked with □.

Important information and special precautions are highlighted in gray.

Abbreviations

C_m : cell membrane capacitance

C_{stray} : electrode stray capacitance

GND: ground

I_{max} : maximal current

R_a : access resistance

R_m : cell membrane resistance

R_{EL} : electrode resistance

SwF: switching frequency

τ_{Cm} : time constant of the cell membrane

V_{REL} : potential drop at R_{EL}

1. Safety Regulations

VERY IMPORTANT: Instruments and components supplied by npi electronic are NOT intended for clinical use or medical purposes (e.g. for diagnosis or treatment of humans), or for any other life-supporting system. npi electronic disclaims any warranties for such purpose. Equipment supplied by npi electronic must be operated only by selected, trained and adequately instructed personnel. For details please consult the **GENERAL TERMS OF DELIVERY AND CONDITIONS OF BUSINESS** of npi electronic, D-71732 Tamm, Germany.

- 1) **GENERAL:** This system is designed for use in scientific laboratories and must be operated only by trained staff. General safety regulations for operating electrical devices should be followed.
- 2) **AC MAINS CONNECTION:** While working with the npi systems, always adhere to the appropriate safety measures for handling electronic devices. Before using any device please read manuals and instructions carefully.
The device is to be operated only at 115/230 Volt 60/50 Hz AC. Please check for appropriate line voltage before connecting any system to mains.
Always use a three-wire line cord and a mains power-plug with a protection contact connected to ground (protective earth).
Before opening the cabinet, unplug the instrument.
Unplug the instrument when replacing the fuse or changing line voltage. Replace fuse only with an appropriate specified type.
- 3) **STATIC ELECTRICITY:** Electronic equipment is sensitive to static discharges. Some devices such as sensor inputs are equipped with very sensitive FET amplifiers, which can be damaged by electrostatic charge and must therefore be handled with care. Electrostatic discharge can be avoided by touching a grounded metal surface when changing or adjusting sensors. **Always turn power off when adding or removing modules, connecting or disconnecting sensors, headstages or other components from the instrument or 19" cabinet.**
- 4) **TEMPERATURE DRIFT / WARM-UP TIME:** All analog electronic systems are sensitive to temperature changes. Therefore, all electronic instruments containing analog circuits should be used only in a warmed-up condition (i.e. after internal temperature has reached steady-state values). In most cases a warm-up period of 20-30 minutes is sufficient.
- 5) **HANDLING:** Please protect the device from moisture, heat, radiation and corrosive chemicals.

2. Introduction

Npi electronic's SEC (Single-Electrode Clamp) systems are based on the newest developments in the field of modern electronics and control theory (see also chapter 8). These versatile current/voltage clamp amplifiers permit extremely rapid switching between current injection and current-free recording of true intracellular potentials.

The use of modern operational amplifiers and an innovative method of capacity compensation makes it possible to inject very short current pulses through high resistance microelectrodes (up to 200 M Ω and more) and to record membrane potentials accurately, i.e. without series resistance error, within the same cycle.

Although the system has been designed primarily to overcome the limitations related to the use of high resistance microelectrodes in intracellular recordings, it can also be used to do conventional whole-cell patch-clamp recordings or perforated patch recordings. The whole-cell configuration allows to investigate even small dissociated or cultured cells as well as cells in slice preparations in both current and voltage clamp mode, while the intracellular medium is being controlled by the pipette solution.

2.1. *Why a Single-Electrode Clamp?*

Voltage clamp techniques permit the analysis of ionic currents flowing through biological membranes at preset membrane potentials. Under ideal conditions the recorded current is directly related to the conductance changes in the membrane and thus gives an accurate measure of the activity of ion channels and electrogenic pumps.

The membrane potential is generally kept at a preselected value (command or holding potential). Ionic currents are then activated by sudden changes in potential (e.g. voltage-gated ion channels), by transmitter release at synapses (e.g. electrical stimulation of fiber tracts in brain slices) or by external application of an appropriate agonist. Sudden command potential changes used to activate voltage-gated currents are especially challenging, because the membrane will adopt the new potential value only after its capacitance (C_m in Figure 1 and Figure 2) has been charged. Therefore, the initial transient current following the voltage step should be as large as possible to achieve rapid membrane charging. In conventional patch-clamp amplifiers, this requires a minimal resistance between the amplifier and the cell interior – a simple consequence of Ohm's law ($\Delta U = R \cdot I$), i.e. for a given voltage difference (ΔU), the current (I) is inversely proportional to the resistance (R). In this context, R is the access resistance (R_a in Figure 1 and Figure 2) between the electrode and the cell interior.

R_a is largely determined by certain electrode properties (mainly electrode resistance) and the connection between the electrode and the cell. Sharp microelectrodes usually have much larger resistances (30 to 150 M Ω or even more) than patch-clamp electrodes. This makes rapid charging of the cell membrane to attain a new voltage level more difficult than in patch-clamp experiments.

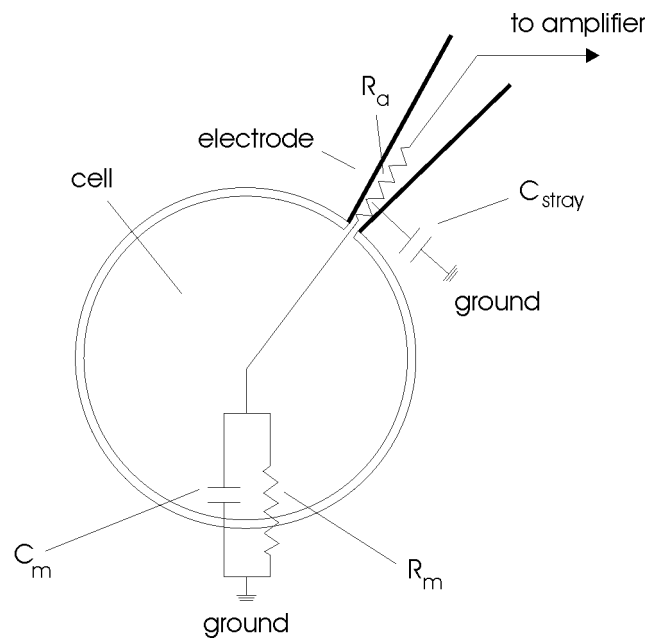


Figure 1: Model circuit for whole-cell patch-clamp recording. C_m : membrane capacitance, C_{stray} : electrode stray capacitance, R_a : access resistance, R_m : membrane resistance

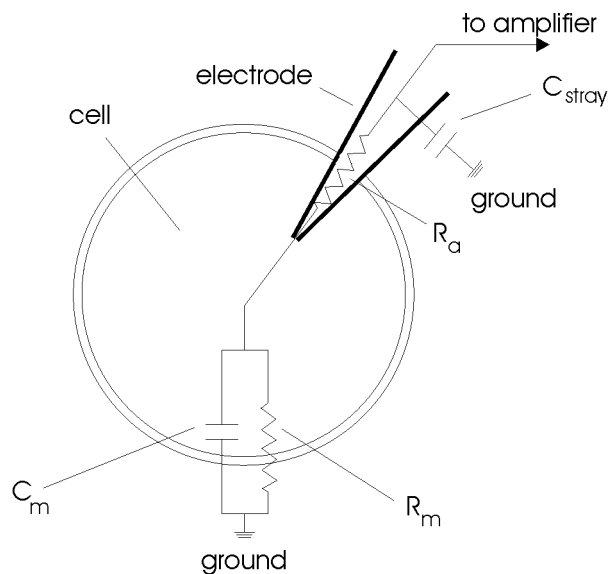


Figure 2: Model circuit for intracellular recording using a sharp electrode. C_m : membrane capacitance, C_{stray} : electrode stray capacitance, R_a : access resistance, R_m : membrane resistance

Besides slowing down the voltage response of the cell, R_a can also cause additional adverse effects, such as error in potential measurement. R_a , together with the membrane resistance (R_m) forms a voltage divider (see Figure 1 and Figure 2). Current flowing from the amplifier to the grounded bath of a cell preparation will cause a voltage drop at both, R_a and R_m . If $R_a \ll R_m$, the majority of the voltage drop will develop at R_m and thus reflect a true membrane

potential. If, in an extreme case, $R_a = R_m$, the membrane potential will follow only one half of the voltage command. In order to achieve a voltage error of less than 1% R_a must be more than 100 times smaller than R_m . This condition is not always easy to accomplish, especially if recordings are performed from small cells. If sharp intracellular microelectrodes are used, it is virtually impossible. If R_a is not negligible, precise determination of the membrane potential can be achieved only if no current flows across R_a during potential measurement. This is the strategy employed in npi electronic's SEC amplifier systems.

The SEC amplifiers inject current and record the potential in an alternating mode (switched mode). Therefore, this technique is called discontinuous SEVC (dSEVC). This ensures that no current passes through R_a during potential measurement and completely eliminates access resistance artefacts.

After each injection of current, the potential gradient at the electrode tip decays much faster than the potential added at the cell membrane during the same injection. The membrane potential is measured after the potential difference across R_a has completely dropped (see chapter 2.2). The discontinuous current and voltage signals are then smoothed and read at the CURRENT OUTPUT and POTENTIAL OUTPUT connectors.

2.2. Principle of Operation

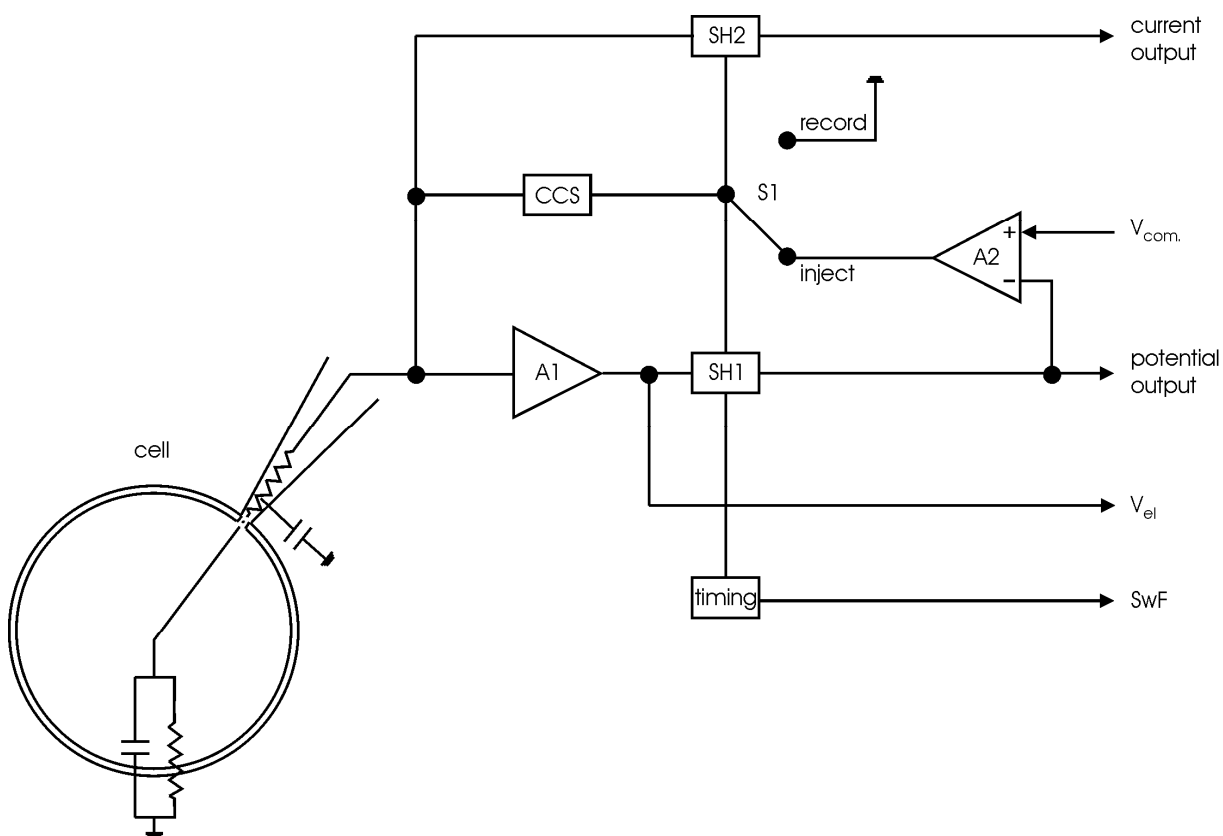


Figure 3: Model circuit of SEC systems

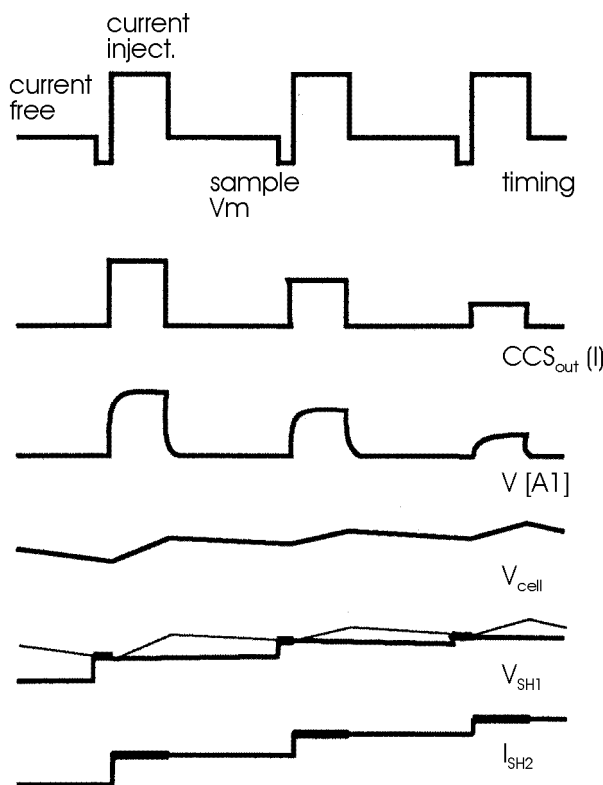


Figure 4: Principle of dSEVC operation

Figure 3 and Figure 4 illustrate the basic circuitry and operation of npI SEC voltage clamp amplifiers.

A single microelectrode penetrates the cell or is connected to the cell interior in the whole-cell configuration of the patch-clamp technique. The recorded voltage is buffered by an $\times 1$ operational amplifier (A1 in Figure 3). At this point, the potential ($V[A1]$ in Figure 4) is the sum of the cell's membrane potential and the voltage gradient which develops when current is injected at the access resistance. Due to npI's unique compensation circuitry, the voltage at the tip of the electrode decays extremely fast after each injection of current and therefore allows for a correct measurement of V_{cell} after a few microseconds. At the end of the current-free interval, when the electrode potential has dropped to zero, the sample-and-hold circuit (SH1 in Figure 3) samples V_{cell} and holds the value for the remainder of the cycle (V_{SH1} in Figure 4).

The differential amplifier (A2 in Figure 3) compares the sampled potential with the command potential (V_{com} in Figure 3). The output of this amplifier becomes the input of a controlled current source (CCS in Figure 3), if the switch S1 (Figure 3) is in the current-passing position. The gain of this current source increases as much as $100 \mu\text{A}/\text{V}$ due to a PI (proportional-integral) controller and improved electrode capacity compensation. In Figure 3 S1 is shown in the current-passing position, when a square current is applied to the electrode. When the current passes the electrode a steep voltage gradient develops at the electrode resistance. V_{cell} (Figure 4) is only slightly changed due to the slow charging of the membrane capacitance. The amplitude of injected current is sampled in the sample-and-hold amplifier SH2 (Figure 3), multiplied by the fractional time of current injection within each duty cycle (1/8 to 1/2 in SEC-05 and SEC-10, 1/4 in SEC-03 systems) and read out as current output (I_{SH2} in Figure 4). S1 then switches to the voltage-recording position (input to CCS is zero). The potential at A1 decays rapidly due to the fast relaxation at the (compensated) electrode capacity. Exact capacity compensation is essential to yield an optimally flat voltage trace at the end of the

current free interval when V_{cell} is measured (see also Figure 13). The cellular membrane potential, however, will drop much slower due to the large (uncompensated) membrane capacitance. The interval between two current injections must be long enough to allow for complete ($\leq 1\%$) settling of the electrode potential, but short enough to minimize loss of charges at the cell membrane level, i.e. minimal relaxation of V_{cell} . At the end of the current-free period a new V_{cell} sample is taken and a new cycle begins. Thus, both current and potential output are based on discontinuous signals that are stored during each cycle in the sample-and-hold amplifiers SH1 and SH2. The signals will be optimal smooth at maximal switching frequencies.

Major advantages of the npi SEC System

Npi electronic's SEC amplifiers are the only systems that use a PI controller to avoid recordings artefacts known to occur in other single-electrode clamp systems ("clamping of the electrode"). The PI controller design increases gain to as much as $100 \mu\text{A}/\text{V}$ in frequencies less than one-fourth the switching frequency. The result is very sensitive control of the membrane potential with a steady-state error of less than 1% and a fast response of the clamp to command steps or conductance changes.

The use of discontinuous current and voltage clamp in combination with high switching frequencies yields five major advantages:

1. The large recording bandwidth allows accurate recordings even of fast signals.
2. High clamp gains (up to $100 \mu\text{A}/\text{V}$) can be used in voltage clamp mode.
3. Very small cells with relatively short membrane time constants can be voltage-clamped.
4. Series resistance effects are completely eliminated for a correct membrane potential control even with high resistance microelectrodes.
5. The true membrane potential is recorded also in the voltage clamp mode (whereas continuous feedback VC amplifiers only reflect the command potential).

3. SEC-05X System

3.1. SEC-05X Components

The following items are shipped with the SEC-05X system:

- ✓ SEC-05X amplifier
- ✓ Headstage
- ✓ GND- and DRIVEN SHIELD (2.6 mm banana plug) connectors

Please open the box and inspect contents upon receipt. If any components appear damaged or missing, please contact npi electronic or your local distributor immediately (support@npielectronic.com).

Optional accessories:

- ⇨ Electrode holder set with one holder for sharp microelectrodes (without port), one patch electrode holder (with one port) and an electrode holder adapter (SEC-EH-SET)



- ⇨ Active cell model (SEC-MODA)
- ⇨ Passive cell model (SEC-MOD, see chapter 6)
- ⇨ Low noise / low bias current headstage (SEC-HSP) with a reduced current range (:10 headstage, i.e. maximal current is ± 12 nA)
- ⇨ Headstage for extracellular measurements (SEC-EXT)
- ⇨ Mini headstage set (SEC-MINI-SE)
- ⇨ Filter for the EPMS system
- ⇨ Data acquisition module
- ⇨ Stimulus isolator module
- ⇨ Iontophoresis module
- ⇨ Pressure ejection module
- ⇨ CellWorks hard- and software

In the following description of the front panel elements each element has a number that is related to that in Figure 5. The number is followed by the name (in uppercase letters) written on the front panel and the type of the element (in lowercase letters). Then, a short description of the element is given. Each control element has a label and often a calibration (e.g. CURRENT OUTPUT, 10 nA/V).

(1) POWER pressure switch



Switch to turn POWER on (switch pushed) or off (switch released).

VOLTAGE CLAMP unit

(2) VC OUTPUT LIMITER potentiometer



Under certain experimental conditions, it is necessary to limit the current in the voltage clamp mode (e.g. in order to prevent the blocking of the electrode or to protect the preparation). This is possible with an electronic limiter, which sets the current range between 0-100%.

(3) VC ERROR display



The VC ERROR display shows the error in the VC (voltage clamp) mode (command minus recorded potential). The desired range of operation is around zero.

(4) GAIN potentiometer



10-turn potentiometer to set amplification factor (GAIN) of the VC error signal. To keep the VC error as small as possible it is necessary to use high GAIN settings, but the system becomes unstable and begins to oscillate if the GAIN is set too high. Thus, the OSCILLATION SHUT-OFF circuit (see #17-19) should be activated when setting this control.

(5) INTEGRATOR TIME CONST. (ms) switch and potentiometer



Potentiometer for setting the INTEGRATOR TIME CONSTANT in VC mode; range: 0.1 to 10 ms, switchable to off-position.

(6) HOLDING POTENTIAL (mV) potentiometer and polarity switch



10-turn digital control that presets a continuous command signal (HOLDING POTENTIAL (XXX mV, maximum: 999 mV) for VC). Polarity is set by switch to the right of the control (0 is off-position).

(7) POTENTIAL FILTER switch



16-position switch to set the corner frequency of the Bessel filter. The setting is monitored by #42.

(8) MODE OF OPERATION switch



The MODE OF OPERATION switch has 6 positions. The active mode of operation is indicated by a red LED next to the operation mode name.

- VCcCC: Voltage Clamp controlled Current Clamp (optional)
- VC: Voltage Clamp
- CC: Current Clamp
- BR: Bridge Mode
- EXT: External Mode
- DHC: Dynamic Hybrid Clamp (optional)

VCcCC mode (optional) (see chapter 8.3)



Voltage Clamp controlled Current Clamp mode. This mode allows accurate current clamp experiments at controlled resting potentials. The time constant is set by the VCcCC TIME CONST. (sec) switch (#51) on the left of the front panel.

BR mode



In the BR (=bridge) mode the electrode resistance is compensated with the BRIDGE BALANCE control (#23). The range can be set to 10 MΩ (100 = 10 MΩ, max resistance 99 MΩ) for low resistance patch microelectrodes or to the range of 100 MΩ (100 = 100 MΩ, max. resistance 999 MΩ) for sharp microelectrodes using a toggle switch (#21).

EXT mode



External mode (see also Figure 6). In external mode CC or VC mode can be selected by a TTL signal applied to the MODE SELECT TTL / DHC TTL connector (#41) below the MODE OF OPERATION switch; TTL low: CC mode, TTL high: VC mode

DHC mode (optional) (see chapter 8.1)



Dynamic Hybrid Clamp mode (see also additional sheet). In DHC mode CC or VC mode is also selected by a TTL signal applied to the MODE SELECT TTL / DHC TTL connector (#41) below the MODE OF OPERATION switch; TTL low: CC mode, TTL high: DHC mode

(9) CURRENT (nA) display



LED-Display for the CURRENT passed through the electrode in nA.

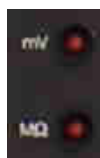
(10) POTENTIAL / RESISTANCE display



LED-Display for the POTENTIAL at the electrode tip in mV or the electrode RESISTANCE in $M\Omega$

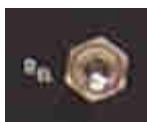
Note: When measuring electrode resistance in LINEAR x10 mode, the reading at the RESISTANCE display (#10) must be multiplied by 10 to obtain the correct value. Example: display reading 01.5 $M\Omega$ means a resistance of 15 $M\Omega$.

(11) mV / $M\Omega$ LEDs



LEDs indicating that POTENTIAL (mV) or RESISTANCE ($M\Omega$) is revealed in display #10

(12) R_{EL} switch



Toggle switch for activating the resistance measurement of the microelectrode. When pushed the microelectrode resistance is measured and shown in the POTENTIAL / RESISTANCE display (#10).

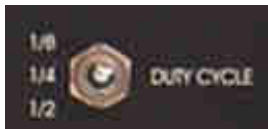
Important: An accurate measurement of R_{EL} requires that the input capacity is well compensated (see also #27 and chapter 7.6)

(13) CURRENT FILTER (Hz) switch



16-position switch to set the corner frequency of the Bessel filter. The setting is monitored by #37.

(14) DUTY CYCLE switch



In the discontinuous modes (VC and CC modes) this switch sets the ratio between current injection and potential recording mode (12.5%; 25% or 50% of each switching period).

(15) SWITCHING FREQUENCY potentiometer



Potentiometer for setting the switching frequency in VC or CC mode; range circa 10 Hz to 70 kHz, indicated on display #40.

(16) CURRENT OUTPUT SENSITIVITY (V/nA) switch



7-position switch to set the CURRENT OUTPUT gain. The setting is monitored by #36.

OSCILLATION SHUT-OFF unit



In SHUTOFF condition the amplifier is set into CC mode and all outputs (including holding current) and CAPACITY COMPENSATION are disabled. The inputs and the ELECTRODE RESISTANCE test are activated.

(17) THRESHOLD potentiometer

Control to set the activation THRESHOLD of the OSCILLATION SHUTOFF circuit potentiometer, linear clockwise, range: 0-1200 mV).

(18) OSCILLATION SHUTOFF LED

Indicates whether the OSCILLATION SHUTOFF circuit is in SHUTOFF condition (LED red) or not (LED green).

(19) DISABLED / RESET switch

Switch to DISABLE the OSCILLATION SHUTOFF unit or to RESET the circuit. A RESET is carried out if one wants to reset the circuit after a previous SHUTOFF condition. After resetting the OSCILLATION SHUT-OFF unit is active again.

PENETRATION / ELECTRODE CLEAR unit

This unit is used to clean the tip of the electrode and to facilitate the puncture of the cell membrane.



(20) PENETRATION push button activates the unit

(22) ELECTRODE CLEAR rotary switch:

- BUZZ mode: overcompensation of the capacity compensation effective in all six modes of operation (VCcCC, VC, CC, BR, EXT, DHC).
- + I_{max} / - I_{max} modes: Application of maximum positive or negative current to the microelectrode (+/- 100 nA, standard headstage).
- OFF

(26) DURATION potentiometer sets duration of pulse

(29) REMOTE TTL connector (active LOW) for connection of a remote switch

(21, 23) BRIDGE BALANCE potentiometer and toggle switch: see #8



(24) HEADSTAGE BIAS CURRENT potentiometer

With this 10 turn potentiometer the output current of the headstage (headstage BIAS current) can be tuned to 0 (see chapter 7.1).

(25) OFFSET potentiometer



Control to compensate the electrode potential (ten-turn potentiometer, symmetrical, i.e. 0 mV = 5 on the dial), range: ± 200 mV (see chapter 7.3).

(26) DURATION potentiometer (see #20)

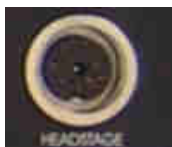
(27) CAPACITY COMPENSATION potentiometer



Control for the capacity compensation of the electrode (ten turn potentiometer, clockwise, range: 0-30 pF, see chapter 7.6).

Caution: This circuit is based on a positive feedback circuit. Overcompensation leads to oscillations that may damage the cell.

(28) HEADSTAGE connector



The HEADSTAGE is connected via a flexible cable and a 12-pole connector to the mainframe (see also chapter 4).

Caution: Please always adhere to the appropriate safety regulations (see chapter 1). Please turn power off when connecting or disconnecting the potential headstage from the POTENTIAL HEADSTAGE connector!

(29) REMOTE TTL connector for PENETRATION unit: see #20

CURRENT CLAMP unit

CURRENT STIMULUS INPUT unit



(30) Toggle switch to activate INPUT #31

(31, 33) BNC connectors for an external CURRENT STIMULUS INPUT in CC mode. Sensitivity: 0.1 nA/V (#31) or 1 nA/V (#33)

(32) Toggle switch to activate INPUT #33



(34) HOLDING CURRENT (nA) potentiometer and polarity switch

10-turn digital control that presets a continuous command signal (HOLDING CURRENT (X.XX nA, maximum: 10 nA) for CC.). Polarity is set by switch to the left of the control (0 is off-position).

(35) CURRENT OUTPUT connector

BNC connector providing the CURRENT OUTPUT signal after passing the CURRENT FILTER (see #13) and the CURRENT OUTPUT SENSITIVITY switch (see #16).

(36) CUR. SENS. MON. +1 V...+7 V

BNC output connector monitoring the setting of CURRENT OUTPUT SENSITIVITY V/ μ A switch (#16). Resolution 1 V / STEP (i.e. 3V indicate a GAIN of 0.5).

(37) FREQ. MON. -8 V...+7 V

BNC output connector monitoring the setting of CURRENT FILTER Hz switch (#13). Resolution 1 V / STEP (i.e. 5 V indicate a filter frequency of 10 kHz).

(38, 39) LINEAR MODE (optional, see chapter 8.2)

Switch (#39) to set the amplifier into the LINEAR mode. The LINEAR mode is indicated by the LINEAR MODE LED (#38) above (green: x1, red: x10).

Note: When measuring in LINEAR x10 mode, several changes to the scaling of displays, inputs and outputs apply. Please see chapter 8.2 for detailed information.

(40) SWITCHING FREQUENCY (kHz) display

LED-Display for the SWITCHING FREQUENCY in kHz in discontinuous VC or CC mode.

(41) MODE SELECT TTL / DHC TTL connector: see #8

(42) FREQ. MON. -8 V...+7 V connector

BNC output connector monitoring the setting of POTENTIAL FILTER Hz switch (#7). Resolution 1 V / STEP (i.e. 5 V indicate a filter frequency of 10 kHz).

(43) POTENTIAL OUTPUT x 10 mV connector



BNC connector monitoring the POTENTIAL at the tip of the electrode (sensitivity: x10 mV).

Important: In LINEAR MODE x10, the voltage output (POTENTIAL OUTPUT x10 mV BNC connector) is set to **x1 mV**, i.e. 1 V is 1 V (and not 100 mV as in LIN mode x1).

VC COMMAND INPUT unit



(44) Toggle switch to activate INPUT #45

(45, 47) BNC connectors for an external COMMAND INPUT in VC mode. Sensitivity: ÷10 mV (#45) or ÷40 mV (#47)

(48) Toggle switch to activate INPUT #47

(46) RISE TIME (ms) potentiometer



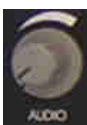
Sometimes it is necessary to limit the rise time of a voltage clamp pulse especially in connection with PI-controllers to avoid overshooting of the potential.

(49) GROUND connector



Banana jack providing the internal GROUND (not connected to PROTECTIVE EARTH).

(50) AUDIO potentiometer



Volume control for the AUDIO MONITOR. The potential at the electrode is monitored by a sound. The pitch of sound is related to the value of the potential.



(51) VCcCC TIME CONST. rotary switch: see #8

3.3. Description of the Rear Panel

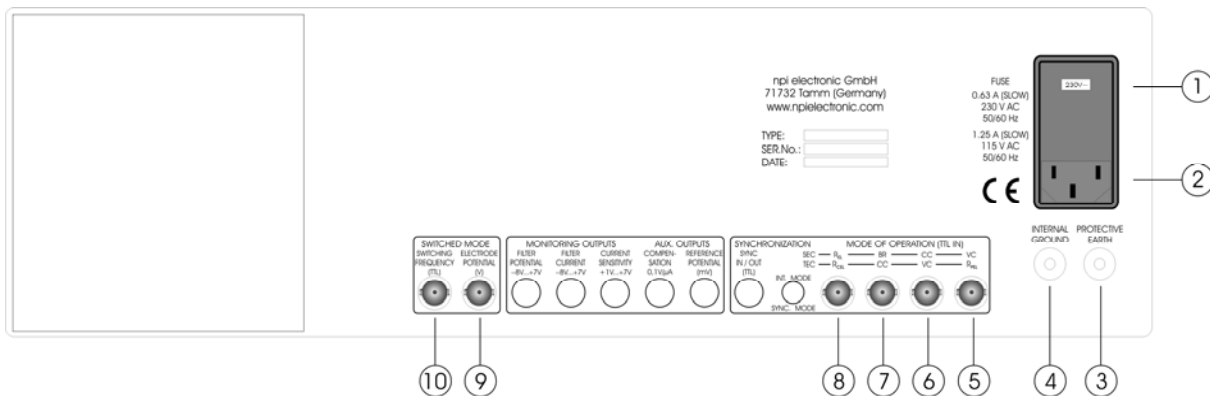


Figure 6: SEC-05X rear panel view (the numbers are related to those in the text below).

(1) FUSE holder

Holder for the line fuse and line voltage selector. For changing the fuse or selecting line voltage open the flap using a screw driver. The fuse is located below the voltage selector. Pull out the holder (indicated by an arrow), in order to change the fuse. For selecting the line voltage, rotate the selector drum until the proper voltage appears in the front.

(2) Mains connector

Plug socket for the mains power-plug.

Important: Check line voltage before connecting the TEC amplifier to power. Always use a three-wire line cord and a mains power-plug with a protection contact connected to ground. Disconnect mains power-plug when replacing the fuse or changing line voltage. Replace fuse only by appropriate specified type. Before opening the cabinet unplug the instrument.

(3) PROTECTIVE EARTH connector

Banana plug providing mains ground (see below).

(4) INTERNAL GROUND connector

Banana plug providing internal ground (see below).

(5-8) MODE OF OPERATION (TTL IN) connectors

BNC connectors for external control of MODE OF OPERATION (see #8, front panel).

(9) ELECTRODE POTENTIAL (V) connector

BNC connector monitoring the electrode potential, i.e. the response of the electrode to the discontinuous current injection.

(10) SWITCHING FREQUENCY (TTL) connector

BNC connector monitoring the selected switching frequency (+5 V pulses), used to trigger the oscilloscope which displays the switching pulses of the ELECTRODE POTENTIAL output #9 (see chapter 7.6)

Grounding

SEC instruments have two ground systems:

1. the internal ground (called INTERNAL GROUND) represents the zero level for the recording electronics and is connected to the recording chamber and the BNC input/output sockets
2. mains ground (PROTECTIVE EARTH) is connected to the 19" cabinet and through the power cable to the protection contact of the power outlet.

GROUND outlets are located on both headstages and on the front panel. For both grounds there is an outlet on the rear panel:

GROUND (black socket): internal system ground
 PROTECTIVE EARTH: (green/yellow socket): mains ground, 19" cabinet

All SEC systems have a high quality toroid transformer to minimize stray fields. In spite of this, noise problems could occur if other mains-operated instruments are used in the same setup. The internal system ground (GROUND sockets) should be connected to only one point on the measuring ground of the recording chamber and should originate from the headstage. The enclosure of the headstage is grounded. Multiple grounding should be avoided and all ground points should originate from a central point to avoid ground loops.

The internal ground and mains ground (= PROTECTIVE EARTH) can be connected by a wire using the ground plugs on the rear panel of the instrument. This connection can be disrupted to avoid "ground loops" (see Ogden, 1994). It is not possible to predict whether measurements will be less or more noisy with the internal ground and mains ground connected. We recommend that you try both arrangements to determine the best configuration.

4. Headstages

4.1. *Standard Headstages*

The SEC-05X comes with the standard headstage (range: ± 120 nA) for connecting glass electrodes with high resistances or patch electrodes for whole-cell patch-clamp recordings with lower resistances via an electrode holder.

A low-noise current headstage for measurement of small currents, a headstage with differential input and a headstage for extracellular measurements is also available (see chapter 4.2).

The electrode filled with electrolyte is inserted into an electrode holder (optional, see Figure 7), which fits into the electrode holder adapter (optional, see also **Optional accessories** in chapter 3.1). The electrical connection between the electrolyte and the headstage is established using a carefully chlorinated silver wire. Chlorinating of the silver wire is very important since contact of silver to the electrolyte leads to electrochemical potentials causing varying offset potentials at the electrode, deterioration of the voltage measurement etc. (for details see Kettenmann and Grantyn (1992)). For optimal chlorinating of silver wires an automated chlorinating apparatus (ACL-01) is available (contact npi for details).

GROUND provides system ground and is linked to the bath via an agar-bridge or an Ag-AgCl pellet. The headstage is attached to the amplifier with the headstage cable (see #1,

Figure 7) and a 12-pole connector. The headstage is mounted to a holding bar that fits to most micromanipulators.

Note: The shield of the SMB connector is linked to the driven shield output and must not be connected to ground. The headstage enclosure is grounded.

Caution: Please always adhere to the appropriate safety precautions (see chapter 1). Please turn power off when connecting or disconnecting the headstage from the HEADSTAGE connector!

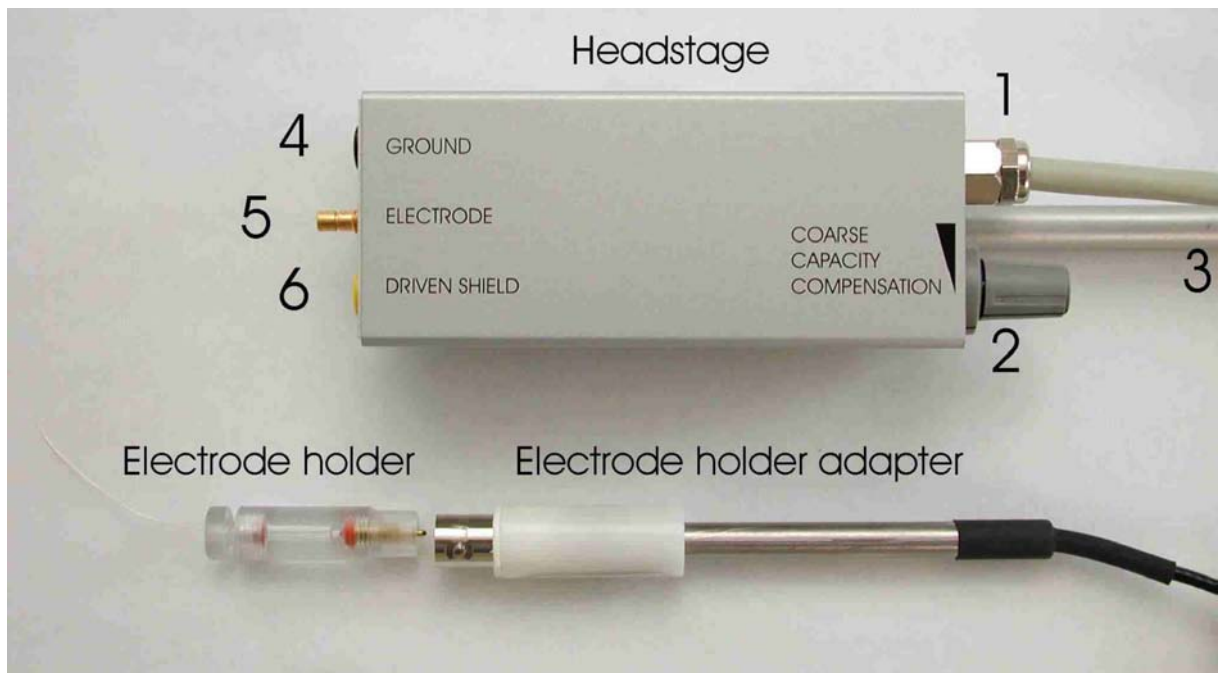


Figure 7: Standard headstage, electrode holder (optional) and electrode holder adapter (optional) of the SEC-05X

The standard headstage consists of the following elements (see Figure 7):

- 1 Headstage cable to amplifier
- 2 Coarse capacity compensation potentiometer
- 3 Holding bar
- 4 GROUND: Ground connector
- 5 ELECTRODE: SMB connector for microelectrode
- 6 DRIVEN SHIELD connector

4.2. Low-noise Headstage (SEC-HSP)

The low-noise / low-bias headstage (range: ± 12 nA, see also **Optional accessories** in chapter 3.1) has an external capacity compensation and a BNC electrode holder connector.

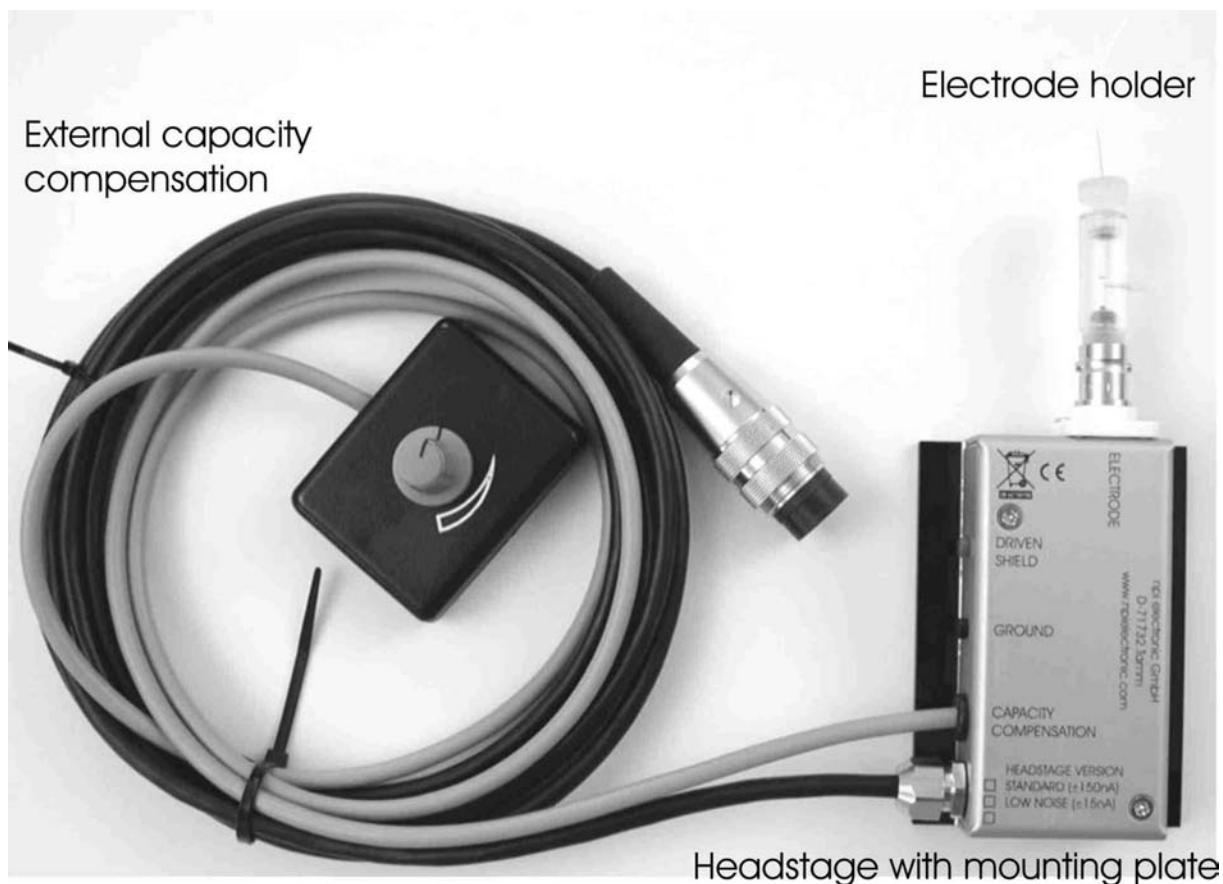


Figure 8: Low-noise headstage with electrode holder (optional)

The headstage is mounted to a non-conducting mounting plate.

Note: The shield of the BNC connector is linked to the driven shield output and must not be connected to ground. The headstage enclosure is grounded.

Caution: Please always adhere to the appropriate safety precautions (see chapter 1). Please turn power off when connecting or disconnecting the headstage from the HEADSTAGE connector!

5. Setting up the SEC-05X System

The following steps should help you set up the SEC-05X correctly. Always adhere to the appropriate safety measures (see chapter 1).

It is assumed that a cell model will be attached.

Electrical connections

- Connect the headstage to the HEADSTAGE connector (#28, Figure 5) at the SEC-05X.
- Connect a cell model (see chapter 6) if you want to test the system with a cell model.
- Connect a digital/analog timing unit or a stimulation device to one of the CURRENT STIMULUS INPUT connectors (#31, #33) for CC experiments and / or to one of the VC COMMAND INPUT connectors (#45, #47) for VC experiments.
- Connect a storage oscilloscope or a data recording device (i.e. a computer with data acquisition card) to the POTENTIAL OUTPUT (#43) and to the CURRENT OUTPUT (#35), triggered from the stimulation device.

Before using the SEC-05X always start with the basic settings to avoid oscillations.

Basic settings

- Turn all controls to low values (less than 1) and the OFFSET (#25) in the range of 5 (zero position, see chapter 7.3).
- Set MODE OF OPERATION (#8) to BR (bridge mode).
- Turn POWER switch (#1) on.

Now the SEC-05X is ready for an initial check with the cell model.

6. Passive Cell Model

The SEC-05X can be ordered with a passive SEC (Single-Electrode Clamp amplifier) cell model as an optional accessory. An active cell model is also available on request (for ref. see Draguhn et al. (1997)).

The cell model is designed to be used to check the function of the SEC amplifier either

1. to train personnel in using the instrument or
2. in case of trouble to check which part of the setup does not work correctly, e.g. to find out whether the amplifier is broken or if something is wrong with the electrodes or holders etc.

The passive cell model consists only of passive elements, i.e. resistors that simulate the resistance of the cell membrane and the electrodes, and capacitances that simulate the capacitance of the cell membrane. A switch allows simulation of two different cell types: a “medium sized” cell with 100 M Ω membrane resistance and 100 pF membrane capacitance, or a “small” cell with 500 M Ω and 22 pF. Electrode immersed into the bath or SEAL formation can be mimicked as well. The headstage of the amplifier can be connected to one of two different types of electrodes (see below).

6.1. Cell Model Description

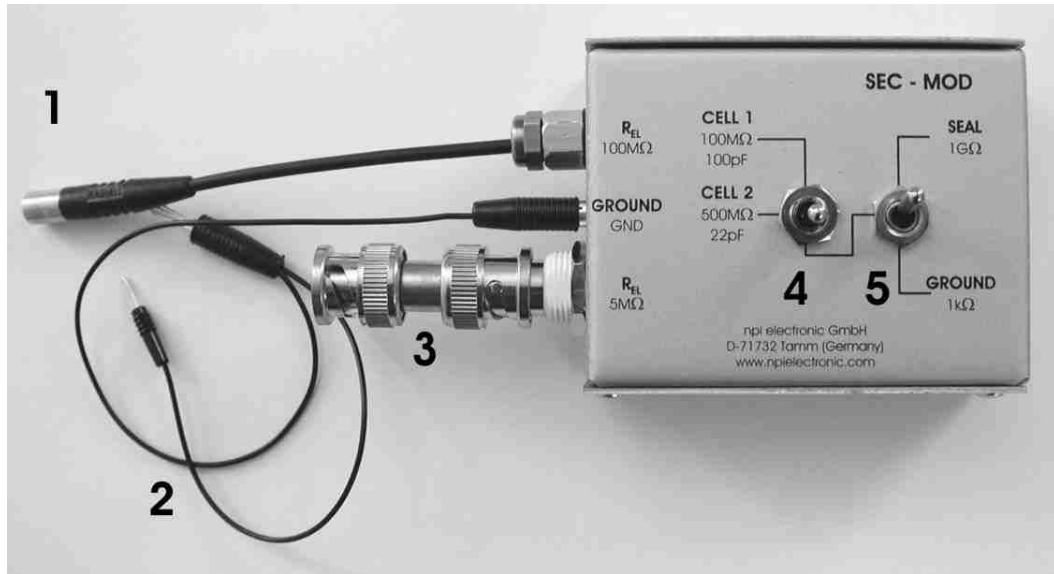


Figure 9: SEC-MOD passive cell model

- 1, 3:** connectors for the headstage, **1:** electrode resistance: 100 MΩ, **3:** electrode resistance: 5 MΩ
- 2:** GND ground connector, to be connected to GND jack of the headstage
- 4:** CELL: switch for cell membrane representing a membrane of either 100 MΩ and 100 pF (CELL 1) or 500 MΩ and 22 pF (CELL 2).
- 5:** In GROUND (lower) position the electrodes are connected to ground via a 1 kΩ resistor. In SEAL (upper) position the electrodes are connected to a 1 GΩ resistor simulating the formation of a GIGASEAL with a patch electrode.

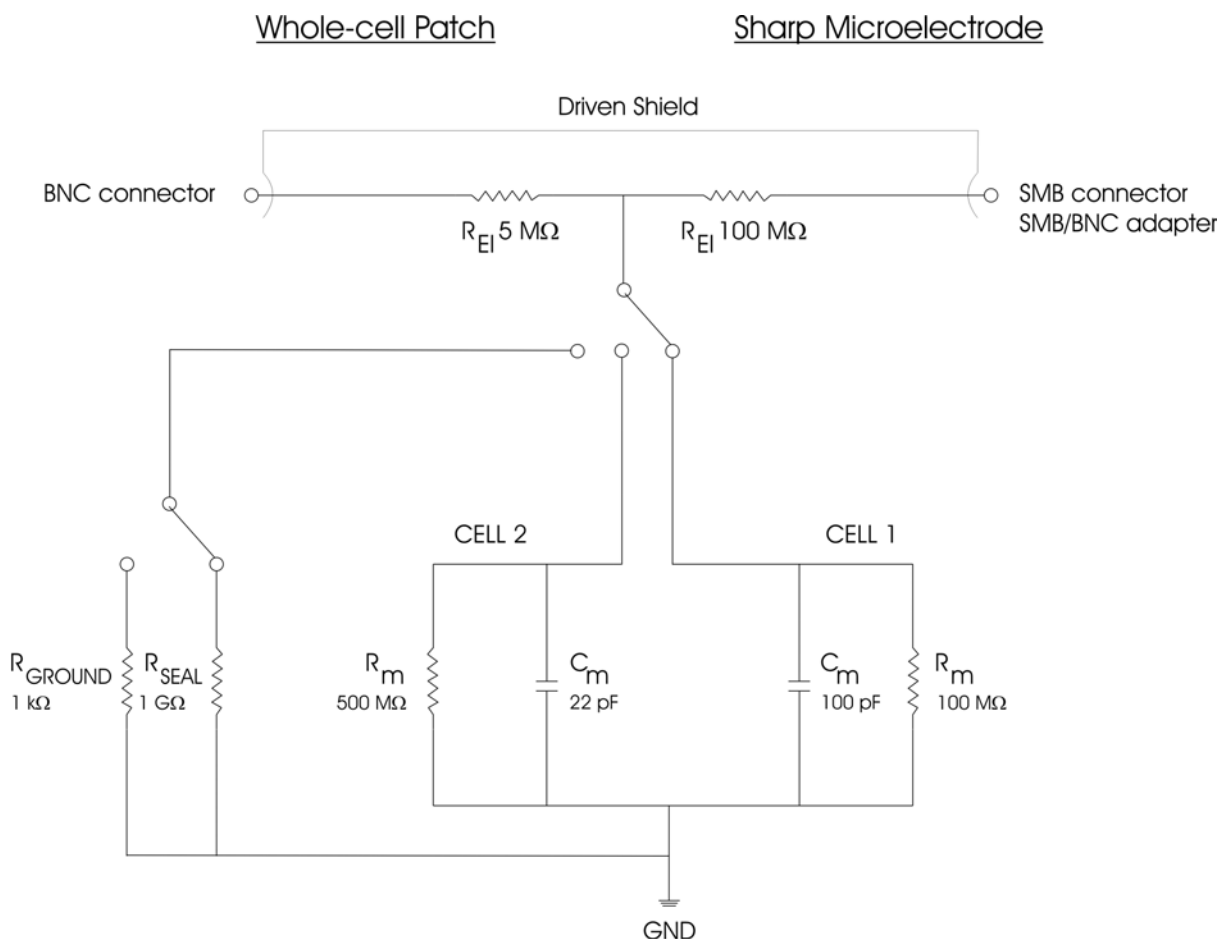


Figure 10: Schematic diagram of the passive cell model

6.2. Connections and Operation

Connections

- Turn POWER switch of the amplifier off.
- a) For simulation of an experiment using a patch electrode
 - Connect the BNC jack of the cell model to the BNC connector P_{EL} of the headstage.
- b) For simulation of an experiment using a sharp electrode
 - Connect the SMB connector of the cell model to the BNC connector P_{EL} at the headstage. For headstages with BNC connector use the supplied SMB to BNC adapter.

For a) and b)

- Connect GND of the cell model to GND of the headstage.
- Do not connect DRIVEN SHIELD

Simulation of electrode in the bath

- Set switch #4, Figure 9 to the lower position.
- Set switch #5, Figure 9 to GROUND position. The 1 k Ω resistor simulates the resistance of the bath solution. This can be used to train cancellation of offsets, using the bridge balance and using the capacity compensation.

Simulation of SEAL formation

- Set switch #4, Figure 9 to the lower position.
- Set switch #5, Figure 9 to SEAL position. The 1 G Ω resistor simulates the SEAL resistance when forming a GIGASEAL in patch-clamp experiments.

Simulation of intracellular recording

Intracellular recordings can be mimicked with one of two cells with different properties. Use the 100 M Ω electrode connector (#1, Figure 9) for an experiment with sharp electrodes or the 5 M Ω electrode connector (#3, Figure 9) for simulating an experiment with patch electrodes.

- Switch the CELL membrane switch (see #4, Figure 9) to the desired position (CELL 1 or CELL 2).
- Turn all controls at the amplifier to low values (less than 1) and the OFFSET in the range of 5 (zero position) and the OSCILLATION SHUTOFF in the DISABLED position.
- Turn POWER switch of the amplifier on.

Now you can adjust the amplifier (see below) and apply test pulses to the cell model. Connection to the BNC jack gives access to the cell via an electrode with 5 M Ω resistance. Connection to SUBCLICK adapter simulates access to the cell via an electrode with 100 M Ω resistance. In the upper position the CELL membrane switch (CELL 1) simulates a cell with a resistance of 100 M Ω and a capacitance of 100 pF. In the lower position (CELL 2) a cell membrane with 500 M Ω and 22 pF is simulated.

7. Test and Tuning Procedures

Important: The SEC-05X should be used only in warmed-up condition i.e. 20 to 30 minutes after turning power on.

The following test and tuning procedures are necessary for optimal recordings. It is recommended to first connect a cell model to the amplifier to perform some basic adjustments and to get familiar with these procedures. It is assumed that all connections are built as described in chapter 5. Many of the tuning procedures can be performed analogue to those described in the manual for the SEC-05LX.

Important: Except for *headstage bias current adjustment* (see 7.1) all adjustments described below should be carried out every time before starting an experiment or after changing the electrode.

7.1. Headstage Bias Current Adjustment

Caution: It is important that this tuning procedure is performed ONLY after a warm-up period of at least 30 minutes!

This tuning procedure is very important since it determines the accuracy of the SEC system. Therefore it must be done routinely with great care.

SEC systems are equipped with a current source that is connected to the current injecting electrode and performs the current injection. This current source has a high-impedance floating output. Therefore the zero position (the zero of the bias current i.e. with no input signal there is no output current) of this device has to be defined.

Since the highly sensitive FET amplifiers in the headstage become warm from the internal heat dissipation and their characteristics are strongly temperature dependent, the calibration procedure has to be done periodically by the user.

The tuning procedure is done in BR Mode using the HEADSTAGE BIAS CURRENT control (#24, Figure 5, range approx. ± 500 pA) and a resistance of a few ten M Ω or a cell model. It is based on Ohm's Law: The voltage deflection caused by the bias current generated by the headstage on a test resistor is displayed on the digital meter. The output current that is proportional to the monitored voltage deflection is tuned to zero with the HEADSTAGE BIAS CURRENT control.

This tuning procedure cannot be performed with an electrode since there always are unknown offset voltages involved (tip potential, junction potentials etc.). Therefore a test resistor of 10-100 M Ω must be used. The procedure is described step by step.

- ❑ First, the headstage electrode connector must be grounded (as if an electrode with a very low resistance were attached). To avoid damage of the headstage amplifiers please use a 10 k Ω resistor (which is small enough compared to a 10-100 M Ω resistor). Now the offset potential of the POTENTIAL OUTPUT can be tuned to zero. Watch the upper digital display and set the POTENTIAL output to zero with the OFFSET control.
- ❑ Next, a resistance of 10-100 M Ω is connected from the headstage output to ground (as if an electrode with a high resistance were attached).
- ❑ The upper digital display (and the POTENTIAL OUTPUT BNC connector (x10mV)) now show a voltage deflection which is proportional to the flowing output current (bias current).
- ❑ This bias current can be tuned to zero with the BIAS control #24. The current is zero when the voltage deflection is zero (i.e. the meter shows zero).
- ❑ As a rule, the current output (CURRENT OUTPUT BNC, #35) and the CURRENT DISPLAY (#9) should also read zero.

Important: All headstages are equipped with very sensitive FET amplifiers, which can be damaged with electrostatic charge and must therefore be handled with care. This can be avoided by touching a grounded metal surface when changing or adjusting the electrodes. If a headstage is not used the input should always be connected to ground (either using an appropriate connector or with aluminum foil wrapped around the headstage). Always turn power off when connecting or disconnecting headstages from the 19" cabinet.

7.2. Electrode Selection

Electrodes must be tested before use. This is done by applying positive and negative current pulses. Electrodes which show significant differences in resistance for current flow of opposite polarity (rectification) cannot be used for intracellular recordings. By increasing the current amplitude the capability of the electrode to carry current can be estimated. The test current must cover the full range of currents used in the experiment. Sometimes the performance of electrodes can be improved by breaking the tip. For further procedures to improve electrode performance, see e.g. Juusola et al. 1997.

7.3. Offset Compensation

If an electrode is immersed into the bath solution an offset voltage will appear, even if no current is passed. This offset potential is the sum of various effects at the tip of the electrode filled with electrolyte ("tip potential", junction potential etc.). This offset voltage must be compensated, i.e. set carefully to zero with the OFFSET control (#25, Figure 5) before recording from a cell. When adjusting the OFFSET make sure that no current flows through the electrode. Thus, it is recommended to disconnect all inputs.

If a cell model is connected the offset compensation should be reached when the OFFSET control reads a value around 5, otherwise it is likely that the headstage or the amplifier is damaged.

7.4. Bridge Balance (in BR mode)

If current is passed through an electrode the occurring voltage deflection (potential drop at R_{EL}) affects the recording of membrane potential in BRIDGE mode. Therefore this deflection must be compensated carefully by means of the BRIDGE BALANCE control (#21,#23). This control is calibrated in $M\Omega$.

With the cell model connected or the electrode in the bath the BRIDGE BALANCE control is turned on clockwise until there is no artifact on the POTENTIAL OUTPUT (see Figure 12).

- Make the basic settings at the amplifier (see chapter 5).
- Connect a cell model or immerse the electrode into the bath as deep as necessary during the experiment.
- Apply current pulses to the electrode either using an external stimulator (via the CURRENT STIMULUS INPUT connectors (#31,33, Figure 5).
- Watch the POTENTIAL OUTPUT at the oscilloscope and adjust the BRIDGE BALANCE as shown in Figure 12 using the BRIDGE BALANCE potentiometer (#23, Figure 5). After adjustment you should see a straight voltage trace without artifacts caused by the potential drop at R_{EL} .

Important: BRIDGE BALANCE must be tuned several times during an experiment since most parameters change during a recording session (see Figure 11)

OFFSET deviations can be detected by comparing the readout on the potential display before and after an experiment (with the electrode in the tissue, but not in a cell).

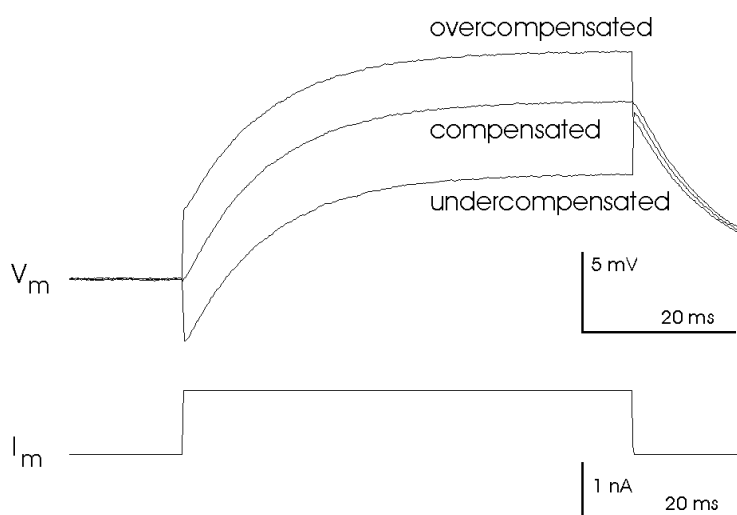


Figure 11: Adjustment of the bridge balance after cell penetration (in BR mode)

Figure 12 illustrates the BRIDGE BALANCE procedure using a $100 M\Omega$ resistor that represents the electrode. The current stimulus amplitude was set to $0.5 nA$. In the upper diagram the bridge is slightly undercompensated and in the diagram in the middle it is slightly overcompensated. The lower diagram shows a well-balanced bridge (compensated).

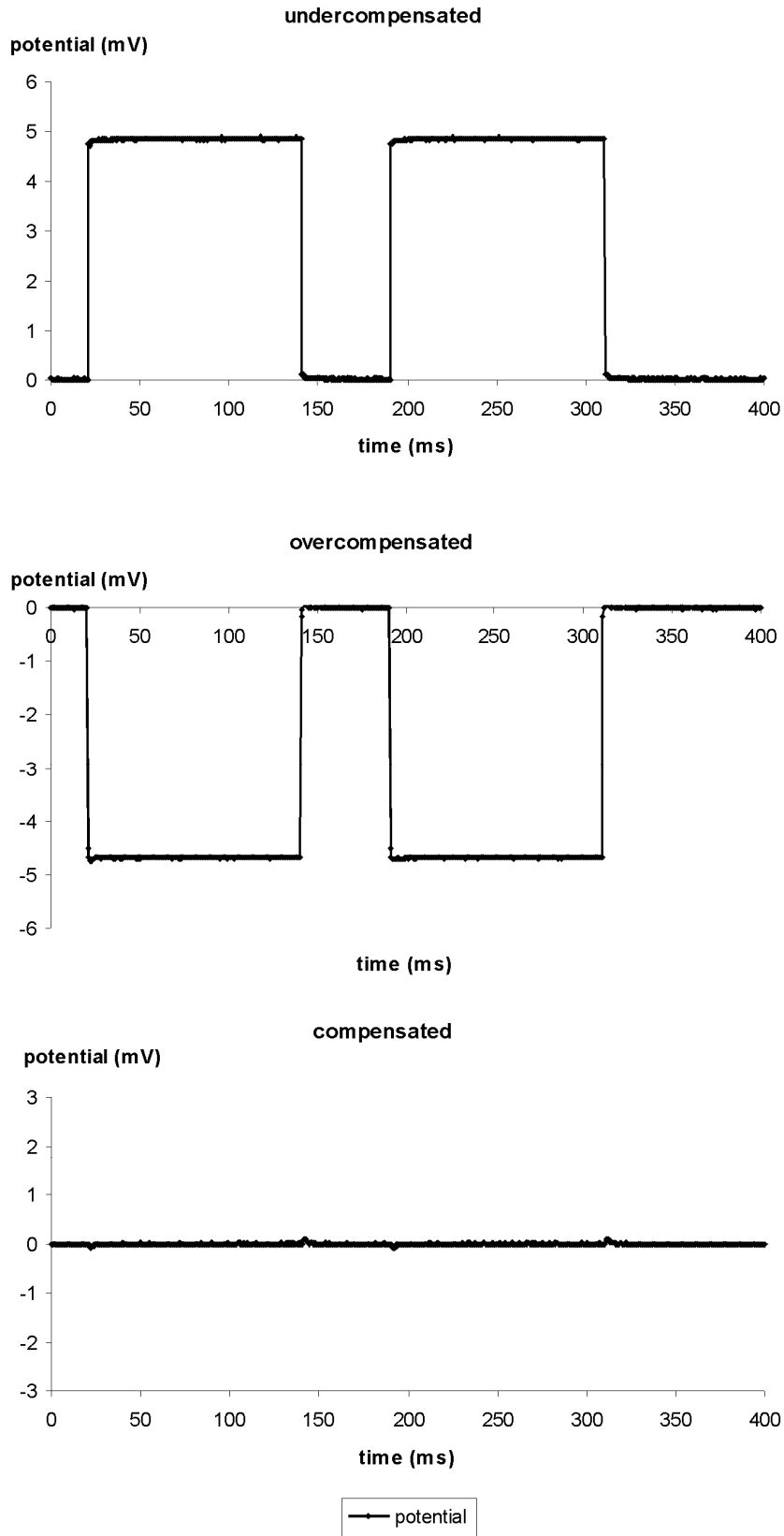


Figure 12: Tuning of the BRIDGE BALANCE using 100 MΩ resistor

7.5. **Switching Frequency and Capacitance Compensation (in switched modes)**

For accurate measurements in switched mode, it is **essential** that the capacity of the electrode is fully compensated.

Important: Wrong compensation of electrode capacity leads to errors in measurements done in switched mode of the amplifier (see Figure 14).

Microelectrode selection: As depicted in chapter 7.2 electrodes must be tested before use. For details see also Richter et al., 1996.

Switching frequency is a key parameter of discontinuous single-electrode clamp (dSEVC) systems. The switching frequency determines the accuracy, speed of response, and signal-to-noise ratio of the dSEVC system (Richter et al., 1996; Muller et al., 1999). Since its launch in 1984, one of the outstanding features of the SEC series of single-electrode voltage / current clamp systems has been the ability to record routinely with high switching frequencies in the range of tens of kilohertz, regardless of the microelectrode resistance (Polder et al., 1984). Principles of the dSEVC technique are described in chapter 2.2 and in (Polder et al., 1984; Polder & Swandulla, 2001).

Looking back: In the early eighties, when the design of the SEC 1L system was started, single-electrode clamping began to gain importance beside the two classical intracellular methods: bridge recording or whole cell patch-clamp recording. The great advantage compared to the whole-cell recording method using a patch amplifier was the elimination of series resistance due to the time sharing protocol (see also chapter 2.2). No current flow during voltage recording means no interference from the series resistance regardless of its value. Thus, voltage-clamp recordings with sharp microelectrodes in deep cell layers became possible. The historical weak point of this method was the low switching frequency due to the fact that stray capacities around the microelectrode could not be compensated sufficiently.

The SEC systems provide a solution for this problem. With their improvements on capacity compensation electronics, they can be used with switching frequencies of tens of kHz even with high resistance microelectrodes. What are the technical principles that make possible such high switching frequencies?

In SEC systems a special protocol is used to rapidly compensate the microelectrode. Figure 13 shows the compensation scheme of a sharp microelectrode immersed 3 mm into the cerebrospinal fluid. Here the increase in speed can be seen clearly. Recordings under such conditions and possible applications have been presented in several papers (e.g. Richter et al., 1996).

Criteria for the selection of the switching frequency

What are the most important criteria for the selection of the switching frequency? This question was analyzed in detail by M. Weckstrom and colleagues (Juusola 1994; Weckstrom et al., 1992). They presented a formula that describes the conditions for obtaining reliable results during a switching single-electrode clamp:

$$f_e > 3f_{sw}, f_{sw} > 2f_s, f_s > 2f_f > f_m$$

- f_e : upper cutoff frequency of the microelectrode
 f_{sw} : switching frequency of the dSEVC
 f_s : sampling frequency of the data acquisition system
 f_f : upper cutoff frequency of the lowpass filter for current recording,
 f_m : upper cutoff frequency of the membrane.

Example (Muller et al., 1999): With the time constant of 1-3 μ s recorded for the electrodes used in this study, f_e is 80-160 kHz, the selected switching frequency of the dSEVC was 30 – 50 kHz (calculated range is 25-53 kHz), data were sampled at 10 kHz and the current signals have been filtered at 5 kHz. Similar settings are currently used for recordings in many labs.

The principle of operation in switched mode is shown below.

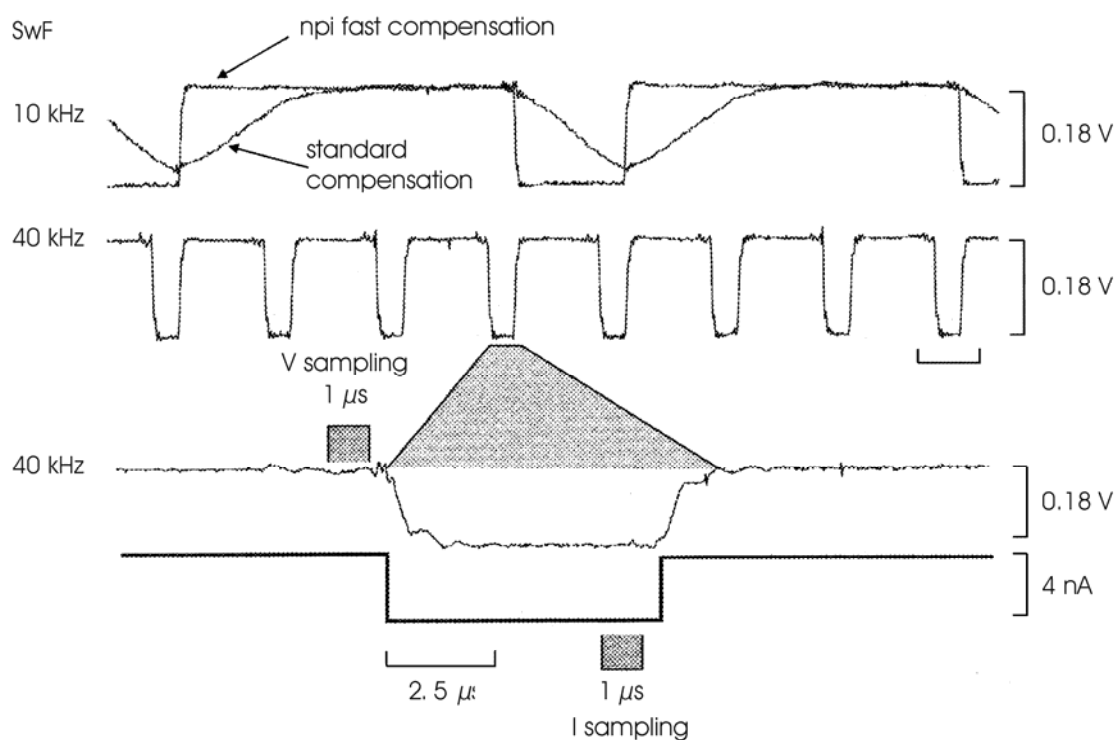


Figure 13: Microelectrode artifact settling

Compensation of stray capacities with a SEC 05 amplifier. The upper trace shows the comparison between the standard capacity compensation and the fast capacity compensation of the SEC systems. After full compensation the settling time of the microelectrode is reduced to a few microseconds allowing very high switching frequencies (here: 40 kHz, middle and lower trace). The microelectrode was immersed 3 mm deep in cerebrospinal fluid. Microelectrode resistance: 45 M Ω , current: 1 nA, duty cycle 1/4. SwF: switching frequency. Original data kindly provided by Prof. Diethelm W. Richter, Goettingen. For details see (Richter et al., 1996).

Important: Artifact-free dSEVC is only possible when the switching frequency and the capacity compensation can be adjusted such that the electrode potential is in a steady state during the sampling intervals. (see Figure 13: Microelectrode artifact settling).

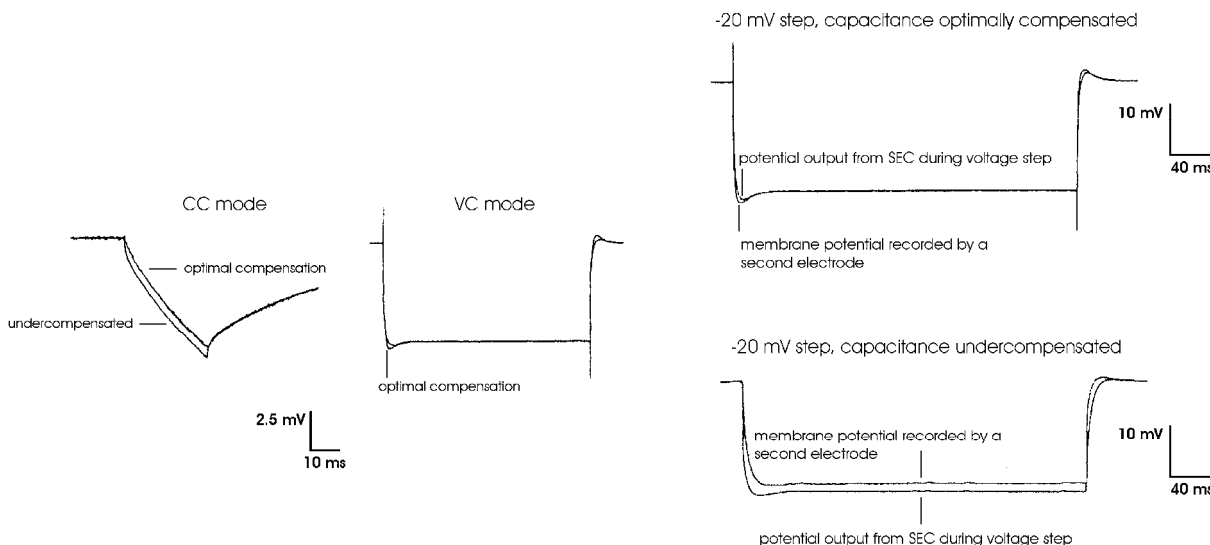


Figure 14: Errors resulting from wrong compensation of the electrode capacity. Original data kindly provided by Ajay Kapur. For details see (Kapur et al., 1998).

7.6. Capacity Compensation - Tuning Procedure

First part: basic setting

In SEC systems the capacity compensation of the electrode is split into two controls, the coarse control at the headstage and the fine control at the front panel of the amplifier. The aim of the first part of the tuning procedure is to set the COARSE CAPACITY COMPENSATION at the headstage, so that an optimal, wide range of CAPACITY COMPENSATION at the amplifier is achieved.

- ❑ Insert the electrode into the electrode holder and connect it to the amplifier.
- ❑ Immerse the electrode, as deep as it will be during the experiment, into the bath solution.
- ❑ Set the CAPACITY COMPENSATION control at the amplifier (potentiometer #27 at the front panel) to a value around 2 and turn COARSE CAPACITY COMPENSATION at the headstage to the leftmost position.
- ❑ Connect the BNC connector ELECT. POTENTIAL OUTPUT (at the rear panel) to an oscilloscope and trigger with the signal at BNC connector SWITCHING FREQUENCY (at the rear panel). The oscilloscope should be in external trigger mode. The time base of the oscilloscope should be in the range of 250 μ s.
- ❑ Set the amplifier in CC mode and select a low switching frequency (1 to 2 kHz)
- ❑ Apply positive or negative current to the electrode using the HOLDING CURRENT control (potentiometer #34 at the front panel).
- ❑ You should see a signal at the oscilloscope similar to that in Figure 15. Turn the COARSE CAPACITY COMPENSATION carefully clockwise until the signal becomes as square as possible (lower diagram in Figure 15).

Important: If you use a model cell (e.g. to train yourself in adjusting the capacity compensation) the capacity of the model cell is always present. Thus, you will get an approximately square shaped signal with a slight slope as shown in Figure 16 (lower panel).

- ❑ Increase the switching frequency to at least 25 kHz. The amplitude and shape of the signal should not change considerably.

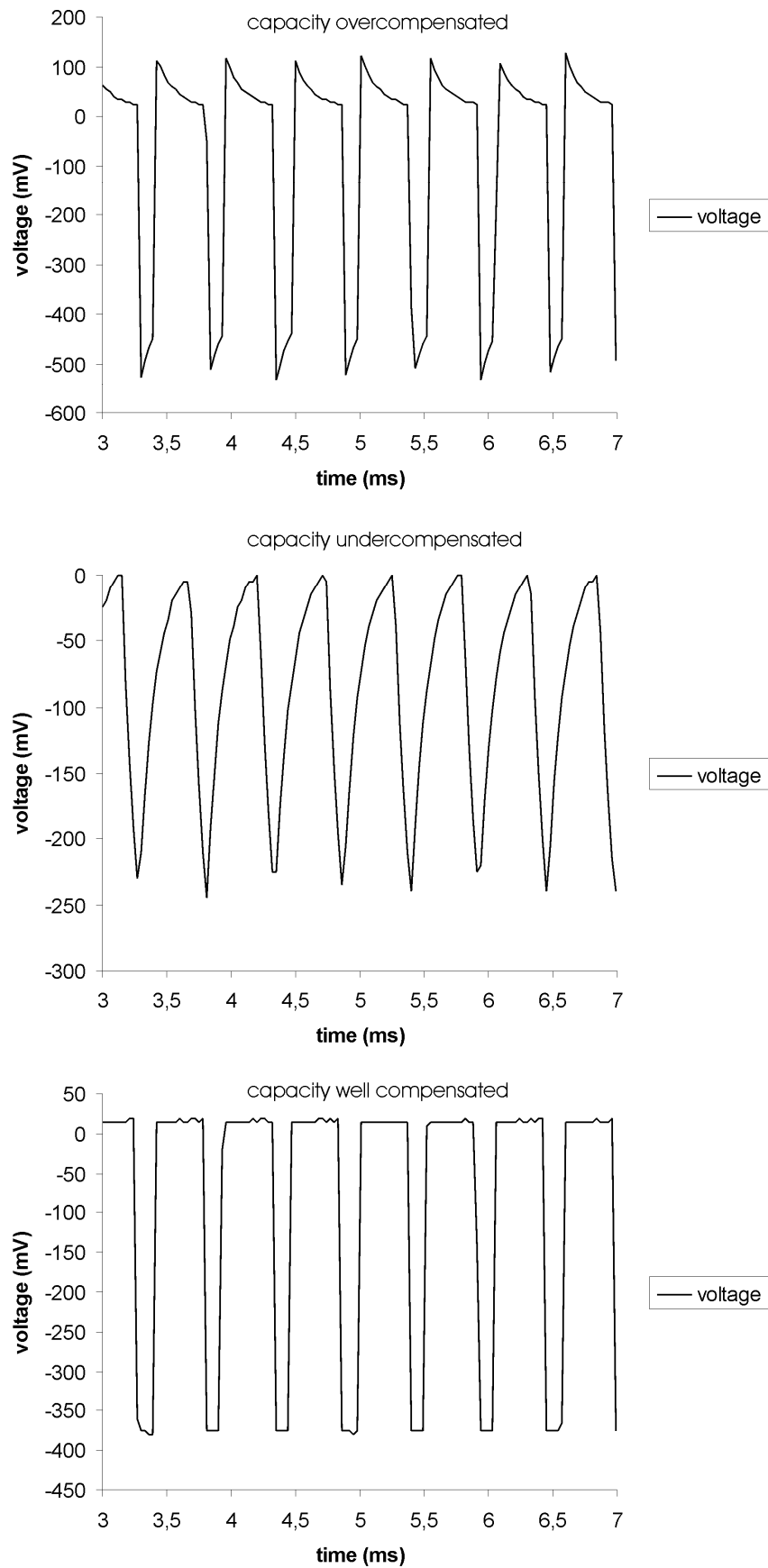


Figure 15: Tuning of the coarse capacity compensation with an electrode (resistance $100\text{ M}\Omega$) in the bath. Time course of the signal at ELECTRODE POTENTIAL OUTPUT (rear panel) is shown (holding current: -1 nA , switching frequency: 2 kHz).

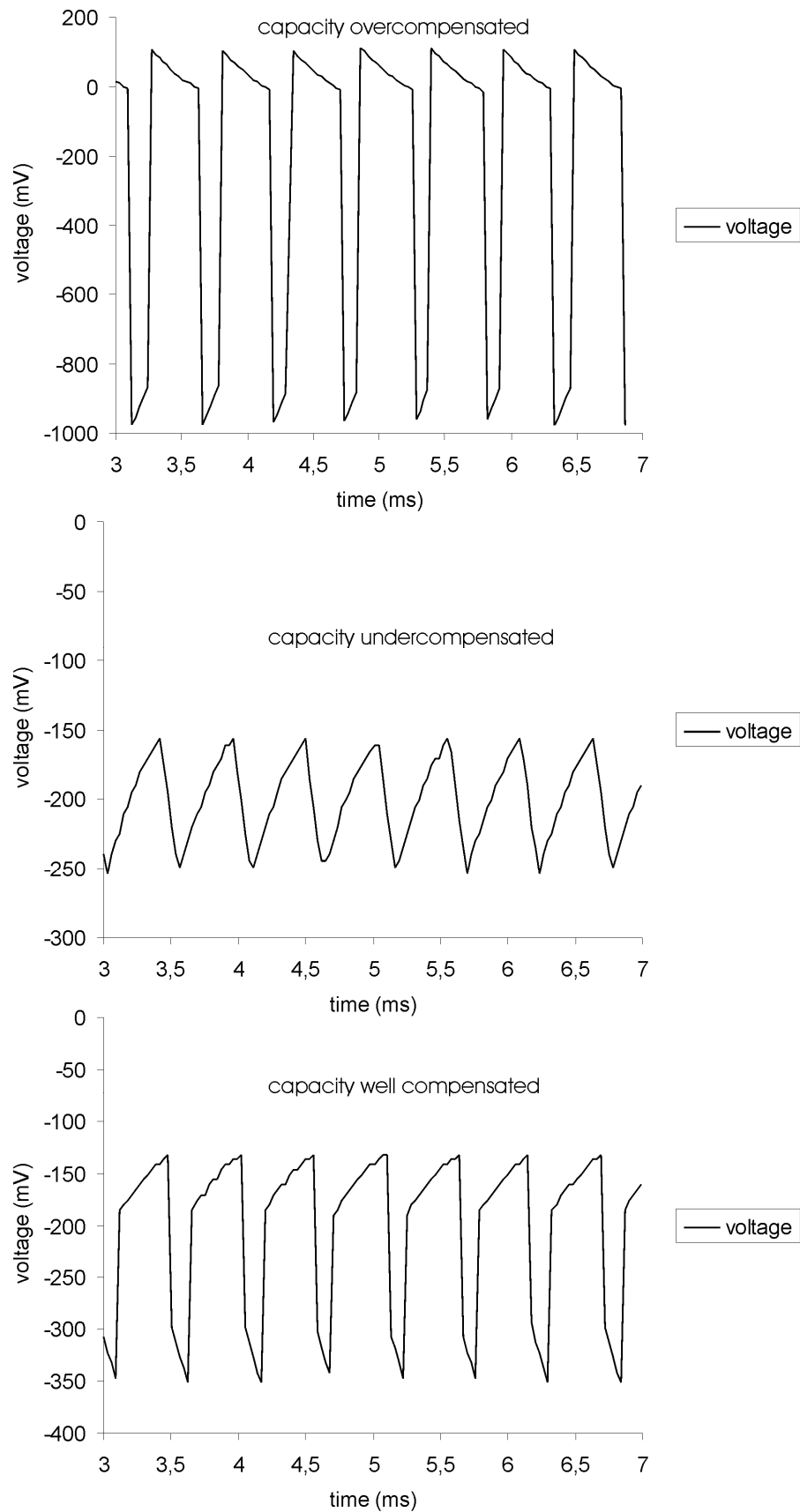


Figure 16: Tuning of the coarse capacity compensation. Time course of the signal at ELECTRODE POTENTIAL OUTPUT (rear panel) is shown (holding current: -1 nA, switching frequency: 2 kHz). A cell model was connected (electrode resistance 100 M Ω).

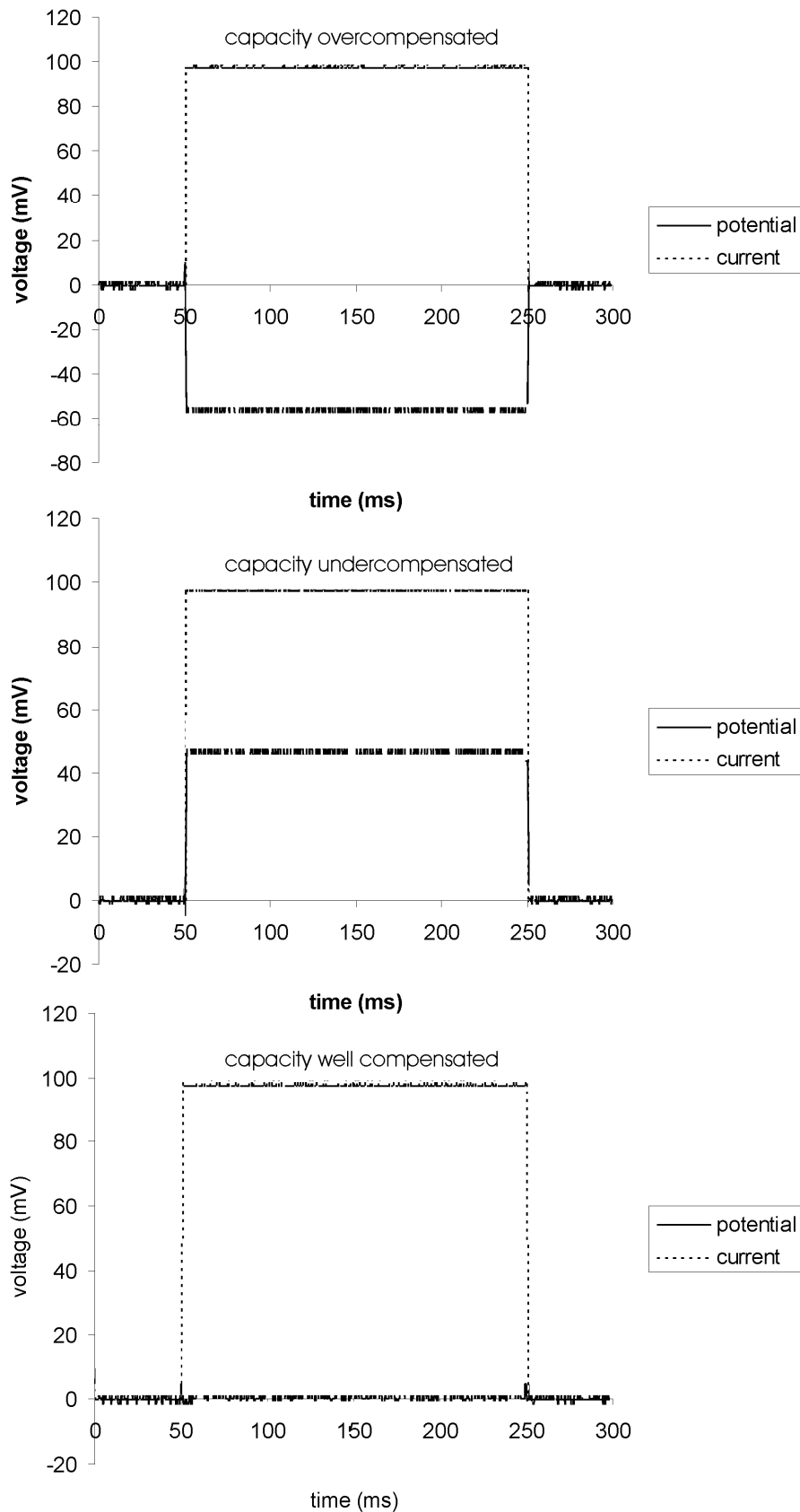


Figure 17: Capacity compensation of the electrode in the bath (electrode resistance: 100 M Ω , Current stimulus: 1 nA, switching frequency: 2 kHz). Current stimulus and electrode potential are shown.

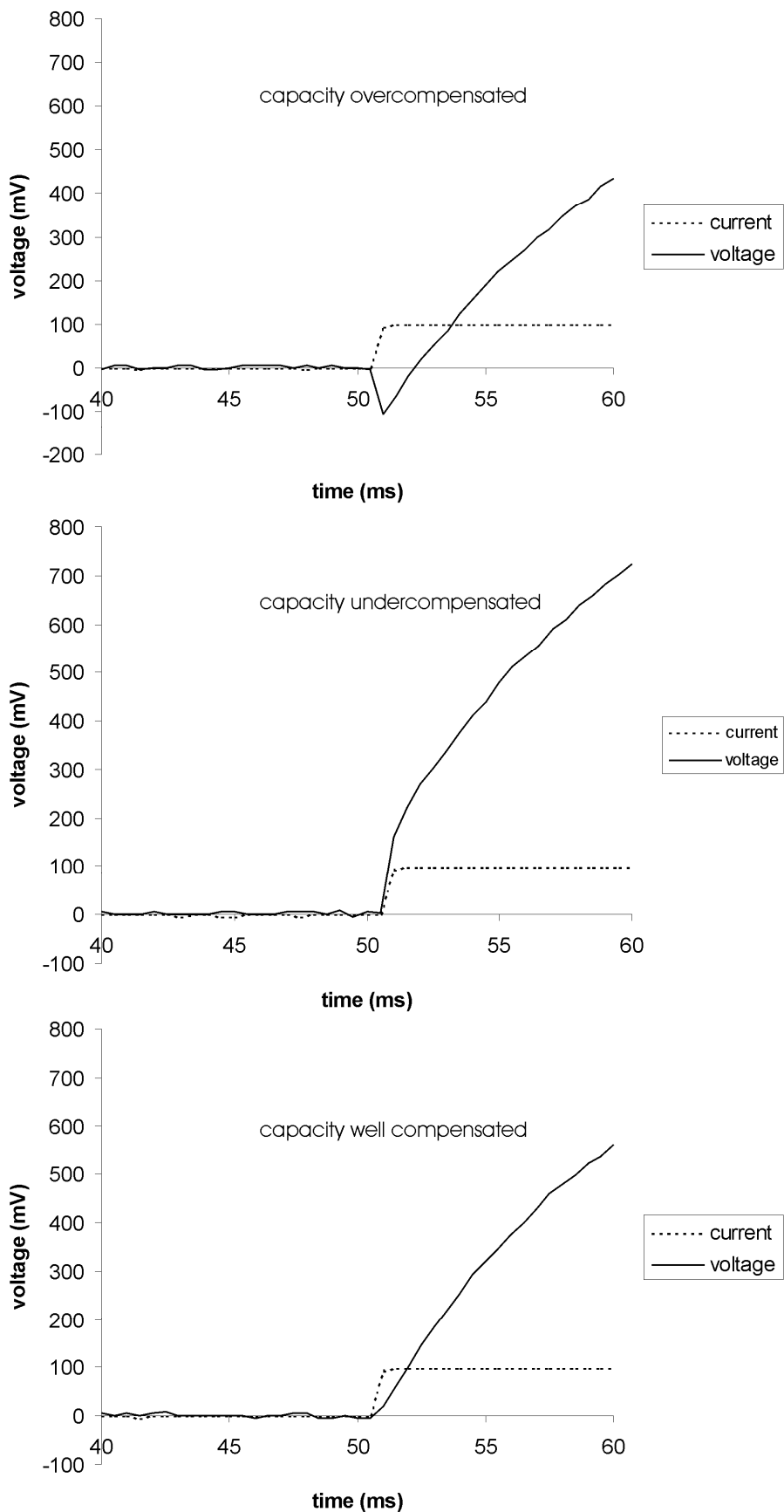


Figure 18: Capacity compensation of the electrode using a cell model (electrode resistance: 100 M Ω , current: 1 nA, cell membrane: 100 M Ω , 100 pF, switching frequency: 2 kHz). Current stimulus and membrane potential are shown.

Second part: fine tuning

Now the basic setting of the CAPACITY COMPENSATION is achieved. Since the electrode parameters change during the experiment (especially after impaling a cell), it is necessary to fine-tune the CAPACITY COMPENSATION during the experiment using the C.COMP. control on the amplifier. To get familiar with this, connect a cell model and go through the following steps (the procedure is the identical with a “real” cell).

- ❑ Connect POTENTIAL OUTPUT and CURRENT OUTPUT (front panel) to another oscilloscope.
- ❑ Set SWITCHING FREQUENCY to the desired value (>25 kHz).
- ❑ Set the HOLDING CURRENT to zero. With the amplifier in CC mode, apply square pulses of a few nA (or a few tens of pA for patch recordings) to the cell. Negative current pulses are recommended. If you apply positive current pulses, be sure only to elicit ohmic responses of the cell membrane, i.e. pulses should not elicit openings of voltage gated channels.
- ❑ The POTENTIAL OUTPUT should show the ohmic response of the cell membrane, without an artifact, as illustrated in Figure 18 and Figure 19.

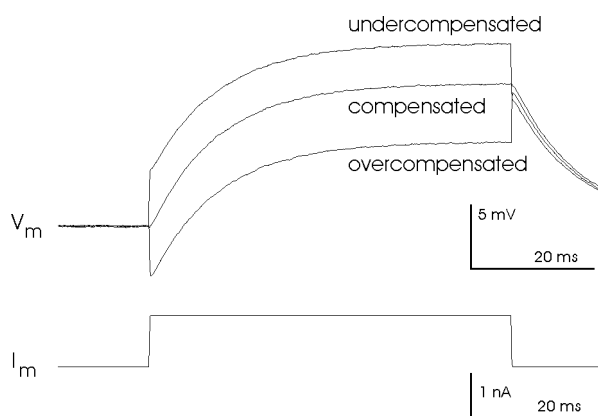


Figure 19: Capacity compensation of the electrode inside a cell (in CC mode). Current stimulus and membrane potential are shown.

Hint: The results of this procedure look very similar to tuning of the bridge balance. If the BRIDGE is balanced accurately no differences in the potential outputs should occur when switching between CC- and BR mode.

Important: Always monitor the OUTPUT from ELECT. POTENTIAL (rear panel), using a second oscilloscope. The signals must be always square. If not, CAPACITY COMPENSATION has to be readjusted or the switching frequency must be lowered.

7.7. Testing Operation Modes

Current Clamp (in BR- or discontinuous CC mode)

The cell's response to current injections is measured in the current clamp (CC) mode. Current injection is performed by means of a current source connected to the microelectrode. Basically the test procedure in BR and CC mode is the same. In the following it is assumed that the basic settings and the tuning procedures are carried out as described in chapters 7.1 to 7.6. All numbers refer to Figure 5.

- Connect the cell model (see 6.2.)
- Set the amplifier to CC or BR mode, respectively, using the MODE OF OPERATION switch (#8).
- Set the membrane resistance of the cell model to 100 M Ω (see chapter 6).
- Set the holding current to -0.5 nA using the HOLD potentiometer (#34) (setting: 50, reading: -0.50 nA) and the HOLD current polarity switch (#34) to “-“.
- Make sure that the ELECTRODE RESISTANCE test R_{EL} (#12) is not active.
- The POTENTIAL display (#10) should read -50 mV (according to Ohm's law). The voltage at POTENTIAL OUTPUT BNC (#43) should be -500 mV.

Remember: The voltage at POTENTIAL OUTPUT is the membrane potential multiplied by 10!

- Apply a test pulse of 0.5 nA to the cell model by giving a voltage step of 0.5 V to CURRENT STIMULUS INPUT (1 nA/V, #33). The length of the test pulse should be at least 30 ms.
- You should see a potential step of 500 mV amplitude at POTENTIAL OUTPUT BNC (#43). Due to the membrane capacity the step is smoothed.

Note: If you expect the POTENTIAL display to show the value of the potential step (in this case $+50$ mV amplitude from a “resting potential” of -50 mV, i.e. -0 mV) remember that the display is rather sluggish and may not display the right value (depending on the length of the step). The same is true for the CURRENT display.

Voltage Clamp

In voltage clamp mode, the membrane potential is forced by a controller to maintain a certain value or to follow an external command. That allows measurement of ion fluxes across the cell membrane. This is the most complex mode of operation with the SEC-05X. Special precautions must be taken while tuning the control circuit in order to avoid stability problems.

- Make sure that the amplifier works correctly with the cell model in CC mode (see above).
- Leave the membrane resistance of the cell model at 100 M Ω .
- Set the holding potential to -50 mV using the HOLD potentiometer (#6, setting: 050, reading: 050 mV) and the HOLD potential polarity switch (#6) to “-“.
- Disable the INTEGRATOR by setting the INTEGRATOR TIME CONST. switch (#5) to OFF.
- Set the GAIN (#4) to 0.1.
- Set the amplifier with the MODE OF OPERATION switch (#8) to VC mode.

- ❑ The upper display should show the holding potential of -50 mV and the lower display the holding current of -0.5 nA (according to Ohm's law).

Hint: If the system oscillates as soon as you switch to VC mode, switch back to CC mode and check the settings. GAIN too high? CAPACITY COMPENSATION not properly adjusted, i.e. overcompensated? INTEGRATOR switch not to OFF?

- ❑ Apply a test pulse of 20 mV to the cell model by giving a voltage step of 0.2 V to VC COMMAND INPUT (#45). The length of the test pulse should be at least 30 ms .
- ❑ You should see a potential step of 200 mV amplitude at POTENTIAL OUTPUT (#43).

Note: If you expect the POTENTIAL display to show the value of the potential step (in this case $+20\text{ mV}$ amplitude from a holding potential of -50 mV , i.e. -30 mV) remember that the display is rather sluggish and may not display the right value (depending on the length of the step). The same is true for the CURRENT display.

8. Special Modes of Operation

8.1. *Dynamic Hybrid Clamp (DHC) Mode (optional)*

General Description

The “**Dynamic Hybrid Clamp**” (DHC) mode is used for investigations of ionic conductances in voltage clamp (VC) mode following action potentials in current clamp (CC) mode. In CC mode an action potential is detected by a spike detector and triggers a timing unit. This timing unit sets a delay for triggering the SEC (being in CC mode). The SEC switches from CC mode to VC mode with the actual membrane potential as holding potential.

Operation

The DHC mode is set through the MODE OF OPERATION switch (#8) at the front panel. When the switch is set to DHC, the amplifier is in CC mode and the membrane potential is fed into sample-and-hold electronics. If a TTL pulse (+5 V) is applied to the MODE SELECT TTL BNC connector (#41), the SEC is switched to VC mode. The COMMAND INPUT for voltage clamp is disabled and the command potential is provided by the sample-and-hold electronics, e.g. the command potential represents the last membrane potential before switching to VC mode.

In practice, the investigator additionally needs a spike detector and a timing unit. The spike detector detects an action potential and triggers – with a delay set by the timing unit – the transition from CC mode to VC mode.

8.2. *Linear Mode (optional)*

General Description

The linear mode of the SEC amplifier is an “unswitched” operation mode of the SEC, working in voltage clamp (VC) and current clamp (CC). In contrast to standard patch-clamp amplifiers the electrode voltage is nevertheless measured, also in VC. However, due to current flow during voltage measurement, this measurement is distorted by the series resistance. This is the reason why the linear mode should be used only for recordings where only little current flows.

In the linear mode the background noise of the amplifier is substantially reduced. Therefore, the linear mode is predestined for low-noise recordings in VC and CC mode.

The linear mode allows also **loose-patch** or **macro-patch** recordings, and can be used to approach the cell and form a giga-seal in VC mode.

The LIN x10 mode can be used for iontophoresis or electroporation, i.e. juxtacellular, non-invasive filling of cells with or single-cell transfection with DNA. The stimulus amplitude range in CC or BRIDGE mode is also enhanced to max. ± 120 nA.

Operation

The linear mode is set through the Linear Mode switch at the front panel. When the switch is set to the middle position, the amplifier is in “switched” (VC or CC) or in BRIDGE mode

(CC). Setting the switch to x1 or x10 lets the amplifier work in linear mode either without or with x10 amplification.

❑ Linear Mode - x1/x10 switch (#39)

- x1: The amplifier operates in linear, unswitched mode (see below), current and/or voltage are not enhanced. LINEAR MODE LED (#38) lights green.
- x10: The amplifier operates in linear electroporation mode. Command voltage in VC or current stimulus in CC or BRIDGE mode are multiplied by the factor of ten. This allows to apply stimuli of max. ± 120 nA. In this operation mode the lights red and the voltage output at POTENTIAL OUTPUT x10mV BNC (#43) connector is set to **x1mV**.
- middle: In the middle position of this switch the amplifier works in switched or BRIDGE mode. The LINEAR MODE LED (#38) does not light.

Important: In LINEAR MODE x10, the voltage output (POTENTIAL OUTPUT x10 mV BNC connector) is set to **x1 mV**, i.e. 1 V is 1 V (and not 100 mV as in LIN mode x1).

Important: The linear mode must be used with low resistance patch pipettes only! Ringing can be avoided by setting the GAIN in VC mode not higher than 1 and by setting the capacity compensation of the electrode to very low values (best close to zero).

Note: Be always aware, that the linear mode introduces a series resistance error that is dependent on the magnitude of series resistance and current that flows during measurement.

Important: The LINEAR mode x1 or x10 **must not be used** if two SEC amplifiers work in synchronized (Master/Slave) configuration.

Important: BRIDGE balance and Capacity Compensation work in LINEAR mode and can be used to minimize artifacts during electroporation.

8.3. VCcCC mode (optional)

General Description

The “Voltage Clamp controlled Current Clamp” (VCcCC) or “slow voltage clamp“ (SLOW VC) mode is used for performing accurate current clamp recordings in the presence of membrane potential oscillations. The npj single and two electrode current and voltage-clamp amplifiers (npj SEC 05X/10LX; npj TEC-05X series) have been modified in a way that slow membrane potential oscillations are exactly controlled by the voltage-clamp module without affecting faster responses, e.g. postsynaptic potentials (PSPs) and action potentials (APs). The response speed of the voltage-clamp feed-back circuit has been decreased by incorporation of electronic circuits with large time constants (1 - 10000 s). In addition, fast current stimuli (e.g. for conductance measurements) can be applied through the current clamp input (CURRENT STIMULUS BNC connector).

Operation

The VCcCC mode is controlled through two front panel elements (located in the VC part of the front panel): the MODE OF OPERATION rotary switch (#8) and a rotary switch to set the time constants (10-100-1000, 5000 and 10000 sec) for the low-pass filter (#51). To start using the VCcCC mode, the amplifier must be tuned accurately in the fast VC mode. The holding potential control must be set to the desired value, or a holding potential signal must be provided from an external device (e.g. a computer). This holding potential will be the preset membrane potential for the VCcCC mode. Under these conditions, PSPs or other changes of the membrane potential will be voltage clamped.

VCcCC mode is activated by switching the MODE OF OPERATION (#8) rotary switch to VCcCC. A red LED indicates its function. Depending on the preset time constant, fast changes of the membrane potential will not be voltage clamped any more. This is a condition that corresponds to an accurate current clamp. Fast changes of the membrane potential are monitored on the potential output, slow changes are compensated by the VCcCC circuit.

The time constant should be selected so that the signals under investigation are not altered by the VCcCC (please compare with current clamp recordings).

Important: The average membrane potential can be changed only through the VOLTAGE COMMAND INPUT. If changes are required, please select a short time constant (1 or 10 s).

Note: Please don't use DHC and VCcCC mode simultaneously!!

Current Clamp Input

The current clamp input (CURRENT STIMULUS BNC connector) is connected in the VCcCC mode in a way that fast current stimuli can be applied to the electrode. **The condition for such recordings is a ratio of >1:1000 between current pulse duration and VCcCC time constant.** Slow (long-lasting) current signals or DC (such as the HOLDING current) will be removed by the action of the VCcCC system. In the fast VC mode, the current clamp input is disconnected automatically. In this way, using the VCcCC mode, fast current stimuli can be used, e.g. to monitor conductance changes.

9. Sample Experiments

In the following the basics of experiments are described either using a sharp or a patch electrode.

It is assumed that all connections are built as described in chapter 5. Before starting remove the cell model.

9.1. Sample Experiment using a Sharp Microelectrode

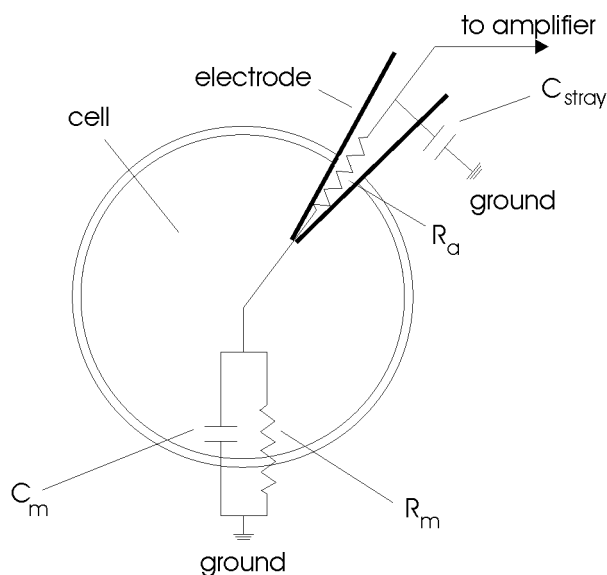


Figure 20: Model circuit for intracellular recording using a sharp electrode

C_m : membrane capacitance, C_{stray} : electrode stray capacitance, R_a : access resistance, R_m : membrane resistance

- Connect the electrode cable / holder to the SMB connector and the Ag-AgCl pellet or the agar-bridge for grounding the bath with GND at the headstage.
- Make the basic settings (see chapter 5).

Again: It is of major importance that SEC-05X systems are used only in warmed-up condition, i.e. 20 to 30 minutes after turning power on.

- Adjust HEADSTAGE BIAS CURRENT to zero if necessary (see chapter 7.1)
- Reconnect the CURRENT STIMULUS INPUT and/or the VC COMMAND INPUT, put an electrode into the electrode holder and attach it to the headstage.
- Immerse the electrode into the bath (not in a cell) as deep as necessary during the experiment. Test the capability of the electrode to carry current (see chapter 7.2), compensate the potential offset (see chapter 7.3), compensate the input capacitance (see chapter 7.6) and measure the electrode resistance (using switch #12, Figure 5).
- Apply current steps to the CURRENT STIMULUS INPUT and adjust the BRIDGE BALANCE to suppress all artifacts on the POTENTIAL OUTPUT (see chapter 7.4).
- Now the system is preadjusted for measurements in BR mode. Find a cell!

- Approach the desired cell. There are several indications that the electrode is very close to the cell membrane:
 - the electrode resistance increases (the bridge balance appears undercompensated)
 - extracellular action potentials (APs) are recorded
 - Apply a BUZZ to the electrode.
 - If you are lucky, the tip of the electrode is now inside the cell.
 - If necessary readjust BRIDGE BALANCE and/or CAPACITY COMPENSATION as shown in Figure 21 and Figure 22 using current stimuli that do not activate ion channels or transporters.
 - You can read the membrane potential and apply current pulses to the cell. After penetration the voltage responses of the cell to the test pulses should reflect the cell membrane resistance and time constant.
 - Start the experiment in BR mode
- or
- Switch to discontinuous CC mode. The shape of voltage and current traces should not change considerably.
 - If you intend to work in discontinuous VC mode, tune the system in CC mode (see above), then switch to VC mode and adjust the clamp as described in chapters 10 and 12.3.

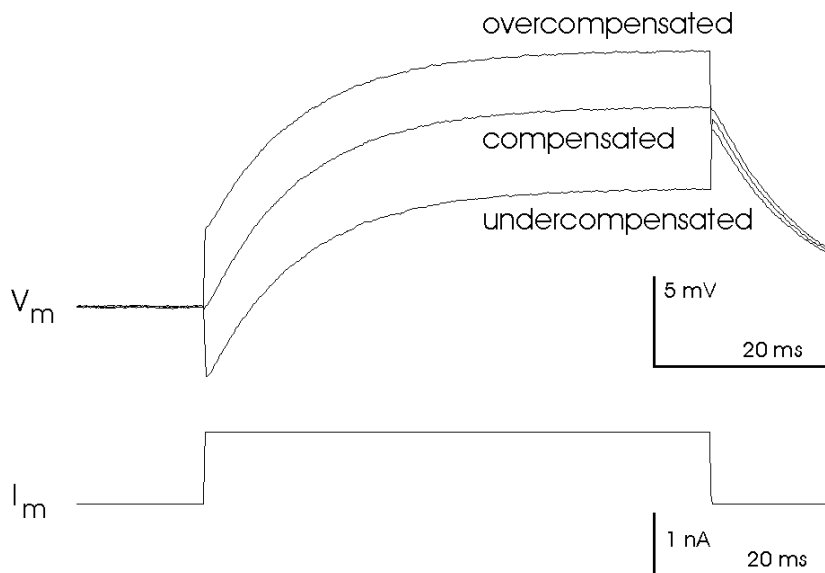


Figure 21: Adjustment of the bridge balance after penetrating a cell

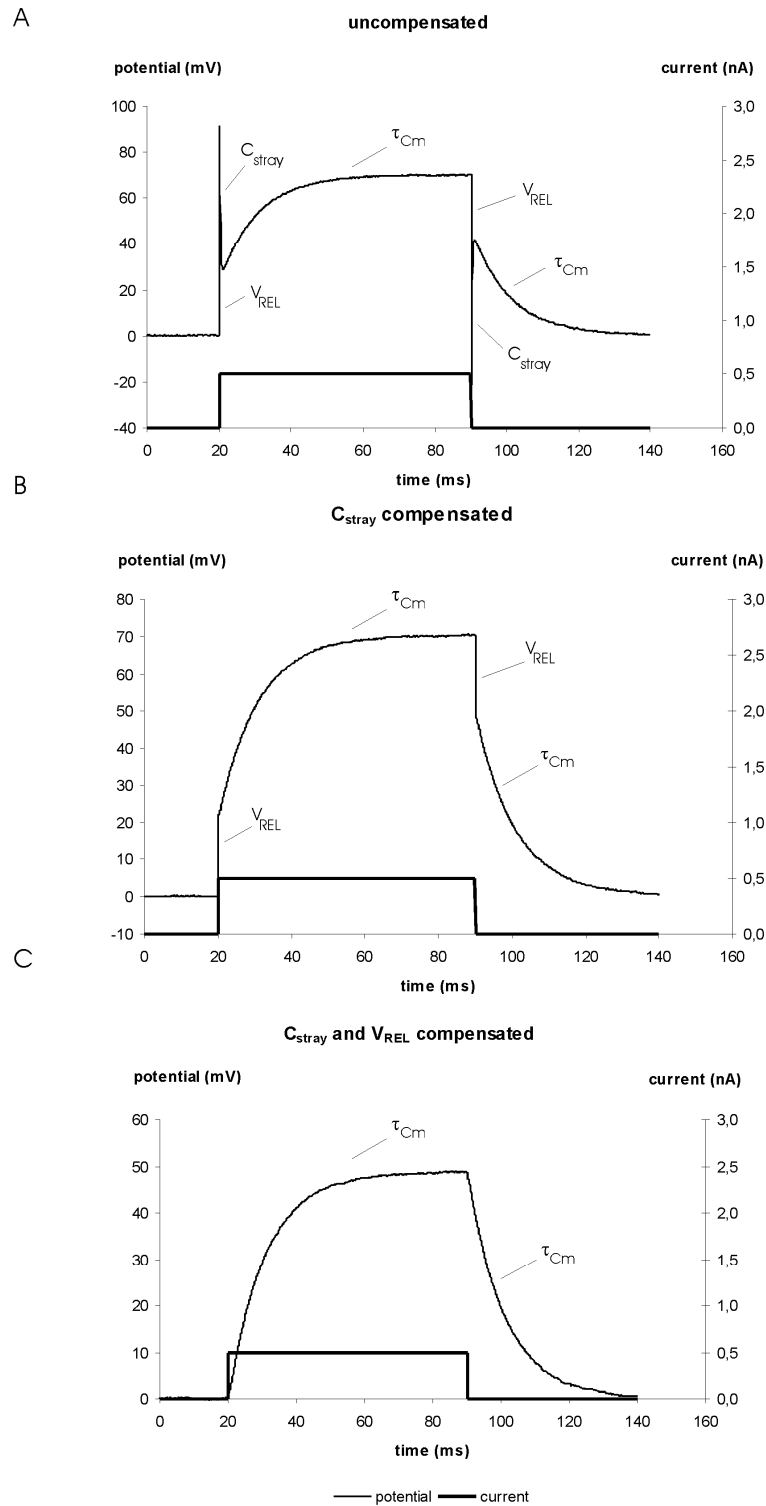


Figure 22: Artifacts caused by the recording electrode. The measurements were done in BR mode using a cell model with 100 MΩ membrane resistance, 100 pF membrane capacitance and 100 MΩ electrode resistance.

A: C_{stray} and V_{REL} not compensated (bridge not balanced)

B: C_{stray}: compensated and V_{REL} not compensated

C: C_{stray} and V_{REL} compensated (bridge balanced)

C_m: membrane capacitance, C_{stray}: electrode stray capacitance, R_{EL}: electrode resistance, R_m: membrane resistance, τ_{Cm}: time constant of the cell membrane, V_{REL}: potential drop at R_{EL} (see also Figure 2)

9.2. Sample Experiment using a Patch Electrode

If patch electrodes are used for whole cell recordings they are usually called “pipettes”. Thus, in this subchapter “pipette” means “patch electrode”.

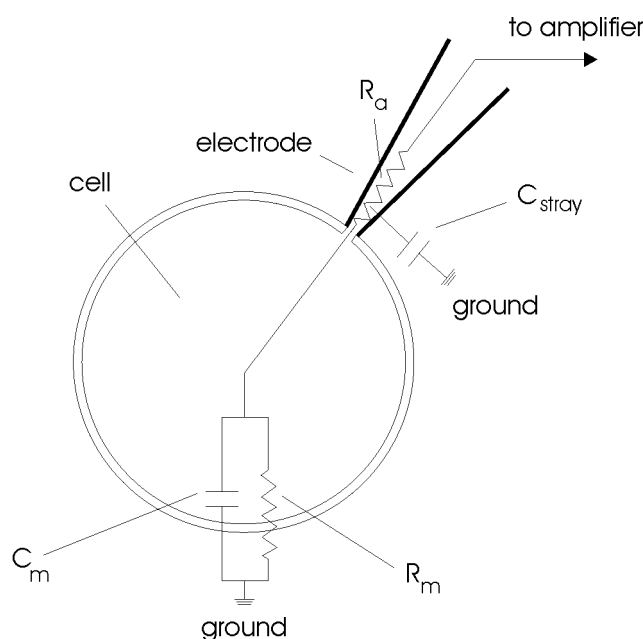


Figure 23: Model circuit for whole cell patch-clamp recording using a patch electrode
 C_m : membrane capacitance, C_{stray} : electrode stray capacitance, R_a : access resistance, R_m : membrane resistance

- ❑ Prepare the setup and proceed as described in the previous subchapter (9.1) until you have selected a cell. Before immersing the pipette into the bath apply slight positive pressure to the pipette to prevent settling of particles at the tip.
- ❑ Apply test pulses to the pipette (about 10 pA). The resulting voltage signals at the pipette are very small (50 μ V with a 5 M Ω pipette).
- ❑ Approach the cell until the voltage signal changes (**a**, Figure 24). Often you can observe a slight dent in the cell membrane.
- ❑ Release pressure from the pipette. Now forming of the seal is indicated by the voltage deflections getting much larger.
- ❑ If the seal does not form apply gentle suction to the pipette until a gigaseal is established (**b**, Figure 24).
- ❑ Apply stronger suction to the pipette or use the BUZZ unit to brake the cell membrane under the pipette opening and establish the whole cell configuration. The whole cell configuration is established if you see the voltage signal getting smaller again (**c**, Figure 24) and you read the expected membrane potential.
- ❑ Read the membrane potential and if necessary, readjust BRIDGE BALANCE and/or CAPACITY COMPENSATION as shown in Figure 21 and Figure 22 using current stimuli that do not activate ion channels or transporters.

Start the experiment in BR mode

or

- Switch to discontinuous CC mode. The shape of voltage and current traces should not change considerably.
- If you intend to work in discontinuous VC mode, tune the system in CC mode (see above), then switch to VC mode and adjust the clamp as described in chapters 10 and 12.3.

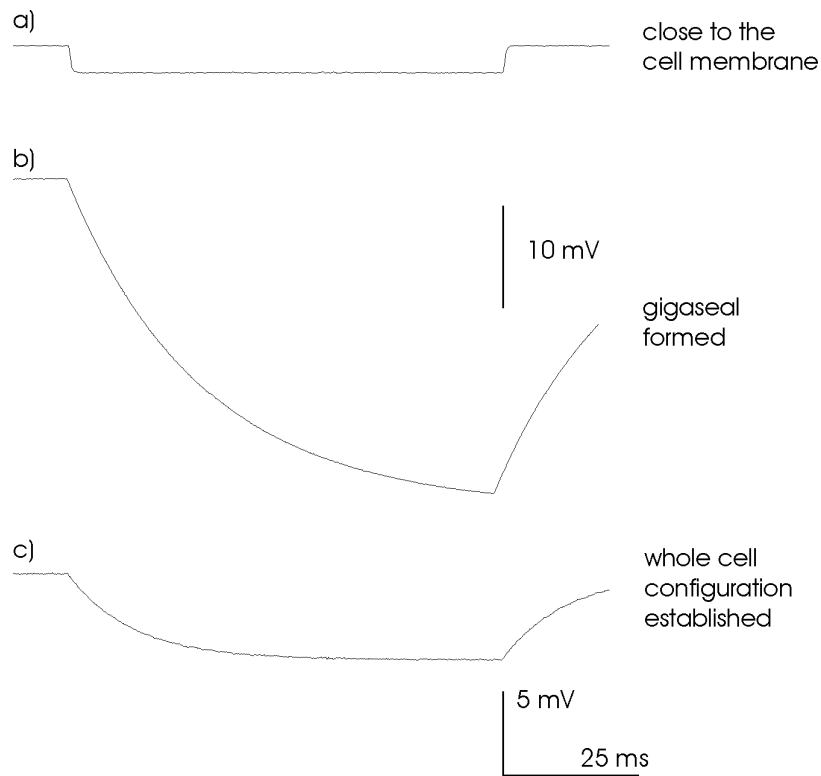


Figure 24: Approaching the cell, forming a gigaseal and establishing the whole cell configuration

10. Tuning VC Performance

A notorious problem in VC mode is the insufficient steepness of voltage steps. . In principle, this problem can be alleviated by tuning the CAPACITY COMPENSATION of the electrode or increasing GAIN to increase clamp speed. However, these tuning procedures may also increase noise. Therefore, the settings of the different parameters result always in a compromise between the stability, accuracy, noise and control speed. In this chapter we will give some practical hints how to optimize the accuracy and speed of the clamp. The theoretical background of adjustment criteria is discussed in chapter 11 (see also Polder and Swandulla, 2001).

The main considerations are: Do I expect rapid or slow responses to voltage changes? How much noise can I accept? Is it possible to use an electrode with low resistance?

General: The speed and accuracy of the voltage clamp control circuit is mainly determined by the magnitude of current flow during injection and by how fast this can happen. Thus, the more current the system can inject within a short time the better the quality of the clamp (see chapter 12.2).






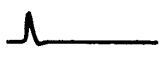
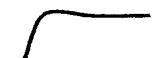
General Considerations

The key to accurate and fast recording is a properly built setup.

- Make sure that the internal system ground is connected to only one point on the measuring ground and originates from the potential headstage. Multiple grounding should be avoided; all ground points should originate from a central point. The electrode used for grounding the bath should have a low resistance and must not produce offsets.
- Try to keep electrode resistances as low as possible.
- Keep cables short.
- Check regularly whether cables and / or connections are broken.
- Make sure that silver wires for the electrodes are properly chlorinated and that there are no unwanted earth bridges, e.g. salt bridges originating from experimental solutions.

SEC systems can be tuned according to one of three optimization methods (see also chapter 12.3):

1. the "linear optimum" (LO) that provides only slow response to a command step and a maximal accuracy of 90-97%.
2. the "absolute value optimum" (AVO) that provides the fastest response to a command step with very little overshoot (maximum 4%) or
3. the "symmetrical optimum" (SO) has the best performance compensating intrinsic disturbance signals but shows a considerable overshoot (maximum 43%) to a step command.

	Response to a command variable step	Response to a disturbance variable step
Linear optimum LO (aperiodic response) P-Controller	 slow response no overshoot	 slow response large deviation
Absolute value optimum AVO PI-Controller	 fastest response 4% overshoot	 slow response slight deviation
Symmetrical optimum SO Unsmoothed command variable PI-Controller	 fast response 43% overshoot	 very fast response slight deviation
Smoothed command variable	 slow response 8% overshoot	

LO

Only a P-Controller is used. The response to a command step is slow and has no overshoot (potential output). The response to a disturbance, e.g. an activating channel, is slow and has a large deviation.

AVO

A PI-Controller is used. The response to a command step is very fast with 4% overshoot (potential output). The response to a disturbance, e.g. an activating channel, is slow and has a slight deviation.

SO

A PI-Controller is used. The response to an unsmoothed command step is fast with 43% overshoot (potential output). The response to a disturbance, e.g. an activating channel, is very fast and has a slight deviation.

Figure 25: Tuning VC according to LO, AVO or SO. The potential output is shown.

Tuning Procedure

Important: First use a cell model for the tuning procedure. You will get familiar with the different settings and the consequences for the system without any damage to cells or electrodes.

- ❑ Before you switch to VC mode tune all parameters related to the recording electrode (offset, capacity compensation etc.) in CC mode, set GAIN to a low, save level and turn INTEGRATOR TIME CONST. (#5, Figure 5) to OFF.
- ❑ Switch to VC mode and apply uniform test pulses to the cell model.
- ❑ The controller is now in P-mode (proportional only). Watch the potential output and increase the GAIN, so that no overshoot appears.
- ❑ Turn the integrator on (INTEGRATOR TIME CONST., #5, Figure 5). The controller is now in PI-mode (proportional-integral). Tune the GAIN again (see above).
- ❑ Watch the potential output and tune the time constant using INTEGRATOR TIME CONST., #5, Figure 5, until the overshoot of the desired tuning method appears (see also Figure 25).

11. Trouble Shooting

In the following section some common problems, possible causes, and their solutions are described.

Important: Please note that the suggestions for solving the problems are only hints and may not work. In a complex setup it is impossible to analyze problems without knowing details. In case of trouble always contact an experienced electrophysiologist in your laboratory if possible, and connect a cell model to see whether the problem occurring with electrodes and “real” cells persists with the cell model.

Problem 1:

After immersing an electrode into the bath there is an unusual high potential offset.

Possible reasons:

1. The Ag-AgCl coating of the silver wire in the electrode holder is damaged
2. The Ag-AgCl pellet or Ag-AgCl coating of the silver wire in the agar-bridge are damaged
3. There is an unwanted GND-bridge e.g. caused by a leaky bath
4. The headstage or the amplifier has an error

Solutions:

1. Chloride the silver wire again
2. Exchange the pellet or chloride the silver wire in the agar-bridge
3. Try to find the GND-bridge and disconnect it e.g. by sealing the bath
4. Contact npj

Problem 2:

Even if no stimulus is given a current flows through the current electrode

Possible reason:

1. The BIAS current is not adjusted

Solution:

1. Adjust the BIAS current according the procedure described in chapter 7.1

Problem 3:

The system oscillates (see also *voltage clamp* in chapter 7.7)

Possible reason:

1. The capacitance of the electrode is overcompensated

Solution:

1. Turn the COARSE CAPACITY COMPENSATION at the headstage and CAPACITY COMPENSATION potentiometer (#27, Figure 5) to the leftmost positions and compensate the input capacitance again (see chapter 7.6)

Problem 4:

With the cell model connected the R_{EL} display does not show the correct value (within a tolerance of 2%).

Possible reason:

1. Electrode capacity is not well compensated
2. The headstage has an error

Solution:

1. Turn the COARSE CAPACITY COMPENSATION at the headstage and CAPACITY COMPENSATION potentiometer (#27, Figure 5) to the most left positions and compensate the input capacitance again (see chapter 7.6)
2. Contact npj

12. Appendix

12.1. Theory of Operation

Voltage clamp instruments are closed-loop control systems with two inputs external to the control loop. An electronic feedback network is used to force the membrane potential of a cell to follow a voltage command (set-point input) as fast and as accurately as possible in the presence of incoming disturbances (disturbance input, correlated with the activities of the cell e.g. activation of ion channels). This is achieved by injecting an adequate amount of charge into the cell. The current injected by the clamp instrument is a direct measure of the ionic fluxes across the membrane (Ferreira et al., 1985; Jack et al., 1975; Ogden, 1994; Smith et al., 1985).

The performance evaluation and optimal tuning of these systems can be done by considering only the command input since the mathematical models (set-point transfer function and the disturbance transfer function, see Froehr, 1985; Polder, 1984; Polder and Swandulla, 1990; Polder, 1993; Polder and Houamed, 1994; Polder and Swandulla, 2001) are closely related. Modern control theory provides adequate solutions for the design and the optimal tuning of feedback systems (Froehr, 1985).

Most voltage clamp systems are composed only of delay elements, i.e. elements which react with a retardation to a change. This type of closed-loop systems can be optimized easily by adequate shaping of the "frequency characteristic magnitude" ($|F(j\omega)|$) of the associated transfer function $F(s)$ (output to input ratio in the frequency domain = **LAPLACE** transform of the differential equation of the system, Polder and Swandulla, 2001).

Using controllers with a proportional-integral characteristic (PI-controllers) it is possible to force the magnitude of the frequency characteristic to be as close as possible to one over a wide frequency range ("modulus hugging", see Froehr, 1985; Polder and Swandulla, 1990; Polder, 1993; Polder and Houamed, 1994; Polder and Swandulla, 2001). For voltage clamps, this means that the controlled membrane potential rapidly reaches the desired command value.

The PI-controller yields an instantaneous fast response to changes (proportional gain) while the integral part increases the accuracy by raising the gain below the corner frequency of the integrator (i.e. for slow signals) to very high values (theoretically to infinite for DC signals, i.e. an error of 0%) without affecting the noise level and stability. Since the integrator induces a zero value in the transfer function, the clamp system will tend to overshoot if a step command is used. Therefore the tuning of the controller is performed following optimization rules which yield a well-defined system performance (AVO and SO, see below).

The various components of the clamp feedback electronics can be described as first or second order delay elements with time constants in the range of microseconds. The cell capacity can be treated as an integrating element with a time constant T_m which is always in the range of hundreds of milliseconds.

Compared to this "physiological" time constant the "electronic" time constants of the feedback loop can be considered as "small" and added to an equivalent time constant T_e . The ratio of the "small" and the "large" time constant determines the maximum gain which can be achieved without oscillations and thus, the accuracy of the clamp. With the gain adjusted to this level the integrator time constant and "small" time constant determine the speed of response of the system.

The clamp performance can be increased considerably if the influence of the current injecting electrode is excluded as far as possible from the clamp loop since the electrode resistance is nonlinear. This is achieved if the output of the clamp system is a current source rather than a voltage source. In this case the clamp transfer function has the magnitude of a conductance (A/V). Another advantage of this arrangement is, that the clamp current can be determined by a differential amplifier with no need of virtual ground.

12.2. Speed of Response of SEC Single-Electrode Clamps

The maximum speed of response of any clamp system to a voltage command step is determined by the cell capacity, the resistance of the current injecting electrode and the maximum output voltage of the VC amplifier (Polder and Houamed, 1994):

$$(dU_m/dt)_{max} = U_{max}/C_m * R_{el} \quad (1) \quad 1 \text{ V} / \text{s} = 10^{-3} \text{ mV} / \mu\text{s} \quad (1a)$$

The standard headstages of the SEC amplifiers are equipped with a current source output with a calibration of 10 nA / V. Therefore with a voltage of ±12 V (linear range of the current source) a maximum current of ±120 nA can be injected into a load of maximum 100 MΩ. In the switched CC or VC modes the maximum current has to be multiplied with the duty cycle (1/8, 1/4, or 1/2). The maximum current is 15 nA, 30 nA or 60 nA.

With the maximum current determined electronically by the current source (for $R_{el} < 100 \text{ M}\Omega$) the maximum speed of response can be calculated as:

$$(dU_m/dt)_{max} = i_{max}/C_m \quad (2)$$

For a given command step U_{com} the shortest time t_r to reach this level can be calculated as:

$$t_r = U_{com}/(dU_m/dt)_{max} \quad (3a)$$

The maximum voltage change ΔU_{max} which can be achieved in a given period of time Δt is:

$$\Delta U_{max} = ((dU_m/dt)_{max} * \Delta t) \quad (3b)$$

Examples:

$C_m = 300 \text{ pF}$, $R_m = 50\text{-}100 \text{ M}\Omega$, (a) $R_{el} = 5 \text{ M}\Omega$, (b) $R_{el} = 100 \text{ M}\Omega$

Equation (2):	$(dU_m/dt)_{max} =$	0.05mV/μs duty cycle = 1/8
		0.1 mV/μs duty cycle = 1/4
		0.2 mV/μs duty cycle = 1/2
Equation (3a):	$t_r =$	1 ms duty cycle = 1/8
($U_{com} = 50 \text{ mV}$)		0.5 ms duty cycle = 1/4
		0.25 ms duty cycle = 1/2

12.3. *Tuning Procedures for VC Controllers*

The initial settings using GAIN only guarantee a stable clamp that is not very accurate and not fast enough for certain types of experiments, e.g. investigation of fast voltage-activated ion channels or gating currents. Thus, for successful and reliable experiments, it is necessary to tune the clamp loop.

Which method one should follow depends on the type of experiment (see below).

- **“Linear Optimum” (LO)**

with this method only the proportional part (GAIN) of the PI controller is used. The response to a command step is slow, but produces no overshoot. The response to a disturbance is also slow with a large deviation of the membrane potential. Clamp accuracy is maximum of 90-97% (Finkel and Redman, 1985¹). Therefore, this method should only be used only if it is very important to avoid overshoots of the membrane potential.

- **"Absolute Value Optimum" (AVO)**

uses the PI controller and provides the fastest response to a command step with very little overshoot (maximum 4%). The response to a disturbance is of moderate speed and the amplitude of the deviation is only half the amplitude obtained with LO. It is applied if maximum speed of response to a command step is desirable, e.g. if large voltage activated currents are investigated.

- **"Symmetrical Optimum" (SO)**

uses also the PI controller and has the best performance compensating intrinsic disturbance signals. The response to a command step shows a very steep rise phase followed by a considerable overshoot (maximum 43%). The response to a disturbance is fast and the amplitude of the deviation is in the same range as with the AVO method. The overshoot can be reduced by adequate shaping of the command pulse by a delay unit (Froehr, 1985; Polder and Swandulla, 1990; Polder and Swandulla, 2001). This method is preferred for slowly activating currents, such as those evoked by agonist application.

The upper speed limit for all optimization methods is determined by the maximum amount of current which the clamp system can force through a given electrode (see chapter 12.2).

Practical Implications

In the following some practical implications of the theory discussed earlier in this chapter are outlined. It is assumed that the system is in VC mode with integrator turned OFF.

Although most of the parameters of the control chain are not known during an experiment, it is possible to tune the clamp controller by optimizing the response to a test pulse applied to the VC COMM. INPUT. The main criterion of tuning is the overshoot seen at the potential

¹ Finkel, A. S. & Redman, S. J. (1985). Optimal voltage clamping with single microelectrode. In *Voltage and Patch Clamping with Microelectrodes*, eds Smith, T. et al, Williams & Wilkins, Baltimore.

output. Since the SO method provides the tightest control it will be most sensitive to parameter settings and requires most experience.

Note: The transitions between the optimization methods are blurred and the tuning procedure is adapted to the experimental requirements. Often, the adequate tuning of a clamp system can be tested by specific test signals (e.g. stimulus evoked signals, etc.).

Very important: All parameters that influence clamp performance (microelectrode offset, capacity compensation, etc.) must be optimally tuned before starting the PI controller tuning procedure.

The tuning procedure involves the following steps:

Again: The main criterion of tuning is the amount of overshoot seen at the potential output.

Tuning of the proportional gain

- Use the command input without smoothing and apply adequate, identical pulses to the cell (e.g. small hyperpolarizing pulses).
- The controller is in P-mode (proportional only). Watch the potential output and rise the GAIN so that no overshoot appears (LO method). The response to a command step is slow and has no overshoot (potential output). The response to a disturbance, e.g. synaptic input or an activating channel, is slow and has a large deviation.

Since the integral part of the controller is disconnected a steady state error in the range of a few percent will be present.

Tuning the integrator

- Reconnect the integrator to form the complete PI controller by turning the INTEGR. potentiometer (#5, Figure 5) on.
- Apply adequate test pulses without filtering.
- Adjust the integrator time constant (#5, Figure 5) to achieve the overshoot of the selected optimization method (4% with the AVO method and 43 % with the SO method). With the AVO method the response to a command step is very fast with 4% overshoot (potential output). The response to a disturbance, e.g. an activating channel, is slow and has a slight deviation. With the SO method the response to an unsmoothed command step is fast with 43% overshoot (potential output). The response to a disturbance, e.g. an activating channel, is very fast and has a slight deviation.

Now the steady-state error must disappear.

Note: If the SO is used, an external command input filter can be used to smooth the command signal and consequently reduce the overshoot according to the requirements of the experiment (see also Figure 25).

13. Literature

13.1. Papers in Journals and Book Chapters about npi Single-electrode Clamp Amplifiers

Recording methods and voltage-clamp technique

- ❑ Dietzel, I. D., Bruns, D., Polder, H. R. & Lux, H. D. (1992). Voltage Clamp Recording, in Kettenmann, H. and R. Grantyn (eds.) *Practical Electrophysiological Methods*, Wiley-Liss, NY.
- ❑ Lalley, P. M., Moschovakis, A. K. & Windhorst, U. (1999). Electrical Activity of Individual Neurons in Situ: Extra- and Intracellular Recording, in: U. Windhorst and H. Johansson (eds.) *Modern Techniques in Neuroscience Research*, Springer, Berlin, New York.
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14. SEC-05X Specifications – Technical Data

MODES OF OPERATION

VCcCC:	Voltage Clamp controlled Current Clamp (discontinuous)
VC:	Voltage Clamp mode (discontinuous)
CC:	Current Clamp mode (discontinuous)
BR:	Bridge Mode (continuous CC)
EXT:	External control mode; the mode of operation can be set by a TTL pulse applied to the MODE SELECT TTL BNC.
DHC:	Dynamic Hybrid Clamp (discontinuous)
MODE selection:	rotary switch, LED indicators; remote selection by TTL

HEADSTAGES

Standard headstage

Operation voltage:	± 15 V
Input resistance:	$<10^{13}$ Ω (internally adjustable)
Current range (continuous mode):	120 nA into 100 M Ω
CC control:	coarse control for input capacity compensation
Electrode connector:	gold plated SUBCLIC (SMB) connector with driven shield
Driven shield output:	2.3 mm connector, yellow, range ± 15 V, impedance 250 Ω
Ground:	2.3 mm connector, black, or headstage enclosure
Holding bar:	diameter 8 mm, length 100 mm
Size:	100x40x25 mm
Headstage enclosure is connected to ground	

Low-noise (whole-cell) headstage (SEC-HSP)

Operation voltage:	± 15 V
Input resistance:	$<10^{13}$ Ω (internally adjustable)
Current range (continuous mode):	12 nA into 100 M Ω
external CC control:	coarse control for input capacity compensation
Electrode connector:	BNC connector with driven shield
Driven shield output:	1 mm connector, red, range ± 15 V, impedance 250 Ω
Ground:	1 mm connector, black, or headstage enclosure
Size:	77x38x20 mm
Mounting plate:	77x54 mm
Headstage enclosure is connected to ground	

Differential input headstage (SEC-HSD)

Operation voltage:	± 15 V
Input resistance:	$<10^{13}$ Ω (internally adjustable)
CMR:	>90 dB
Current range (continuous mode):	120 nA into 100 M Ω
CC control:	coarse control for input capacity compensation
Electrode connectors:	two gold plated SUBCLIC (SMB) with driven shields
Driven shield output:	2.3 mm connector, yellow, range ± 15 V, impedance 250 Ω
Ground:	2.3 mm connector, black, or headstage enclosure
Holding bar:	diameter 8 mm, length 100 mm
Size:	100x40x25 mm
Headstage enclosure is connected to ground	

ELECTRODE PARAMETER CONTROLS

Offset: ten-turn control, ± 400 mV
 Capacity compensation: range 0-30 pF
 adapts compensation circuit to electrode parameters
 coarse control at headstage
 fine control at front panel: ten-turn potentiometer

BANDWIDTH and SPEED OF RESPONSE

Full power bandwidth ($R_{EL} = 0$): >100 kHz
 Rise time (10-90%, $R_{EL} = 100$ M Ω): <10 μ s
 Rise time (10-90%, $R_{EL} = 10$ M Ω): <8 μ s
 Electrode artifact decay (switched modes, 10 nA signal):
 <500 ns ($R_{EL} = 10$ M Ω); <1.5 μ s ($R_{EL} = 100$ M Ω)
 CAPACITY COMPENSATION tuned with no overshoot.

ELECTRODE RESISTANCE TEST

obtained by application of square current pulses ± 1 nA; ± 10 mV/M Ω at the POTENTIAL OUTPUT; display XXX M Ω

SWITCHED MODE PARAMETERS

Switching frequency: linear control, ca. 10-70 kHz;
 Duty cycle: 1/8, 1/4, 1/2 (12.5%, 25%, 50% current injection)

CURRENT RANGE in SWITCHED MODE with duty cycle of 1/2 (1/4, 1/8)

Standard headstage: ± 60 nA (± 30 nA, ± 15 nA)
 SEC-HSP headstage: ± 6 nA (± 3 nA, ± 1.5 nA)

SWITCHED MODE OUTPUTS

Electrode potential: max. ± 12 V, output impedance: 250 Ω
 Switching frequency: TTL, output impedance: 250 Ω

CURRENT OUTPUT

0.1 nA/V...10 nA / V, selectable by rotary switch
 output impedance: 0 Ω ; current display: X.XX nA

POTENTIAL OUTPUT

Sensitivity: $\times 10$ mV; output impedance: 0 Ω ; potential display: XXX mV

CURRENT CLAMP

Inputs: 1 nA/V, 0.1 nA/V; input resistance: >100 k Ω
 HOLD: X.XX nA ten-turn digital control with -/0/+ switch, max. 10 nA
 BRIDGE balance: ten-turn digital control together with range toggle switch:
 10 M Ω position: XX.X M Ω
 100 M Ω position: XXX M Ω
 Noise (BRIDGE MODE): 400 μ V_{pp} / 1 pA_{pp} with 100 M Ω resistance at 10 kHz bandwidth

VOLTAGE CLAMP

Input: ± 10 mV, ± 40 mV; input resistance >100 k Ω
 HOLD: XXX mV, ten-turn digital control with +/0/- switch, max. 1000 mV
 GAIN: 100 nA/V - 10 μ A/V ten-turn linear control
 Noise: potential output: <500 μ V_{pp}, current output: <300 pA_{pp}

measured at a cell model

($R_{EL} = 100\text{ M}\Omega$, $R_m = 100\text{ M}\Omega$, $C_m = 100\text{ pF}$, duty cycle = 25%,
switching frequency = 40 kHz, standard headstage, 10 kHz bandwidth)

potential output: $<500\text{ }\mu\text{V}_{pp}$, current output: $<200\text{ pA}_{pp}$

measured at a cell model

($R_{EL} = 5\text{ M}\Omega$, $R_m = 500\text{ M}\Omega$, $C_m = 22\text{ pF}$, duty cycle = 25%, switching
frequency = 40 kHz, standard headstage, 10 kHz bandwidth)

RESPONSE SPEED: Rise time (10-90%): $<350\text{ }\mu\text{s}$

for 50 mV step applied to a cell model

($R_{EL} = 100\text{ M}\Omega$, $R_m = 100\text{ M}\Omega$, $C_m = 100\text{ pF}$, duty cycle = 25%,
switching frequency = 30 kHz, standard headstage, 10 kHz bandwidth)

Rise time (10-90%): $<100\text{ }\mu\text{s}$

for 50 mV step applied to a cell model

($R_{EL} = 5\text{ M}\Omega$, $R_m = 500\text{ M}\Omega$, $C_m = 22\text{ pF}$, duty cycle = 25%, switching
frequency = 30 kHz, standard headstage, 10 kHz bandwidth)

POWER REQUIREMENTS:

115/230 V AC, 60 W (1.25 A/0.63 A fuse, SLOW).

DIMENSIONS:

19" rack-mount cabinet, 19" (483 mm) wide, 14" (355 mm) deep, 5.25" (132.5 mm) high,
weight: approx. 9 kg.

15. Index

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